

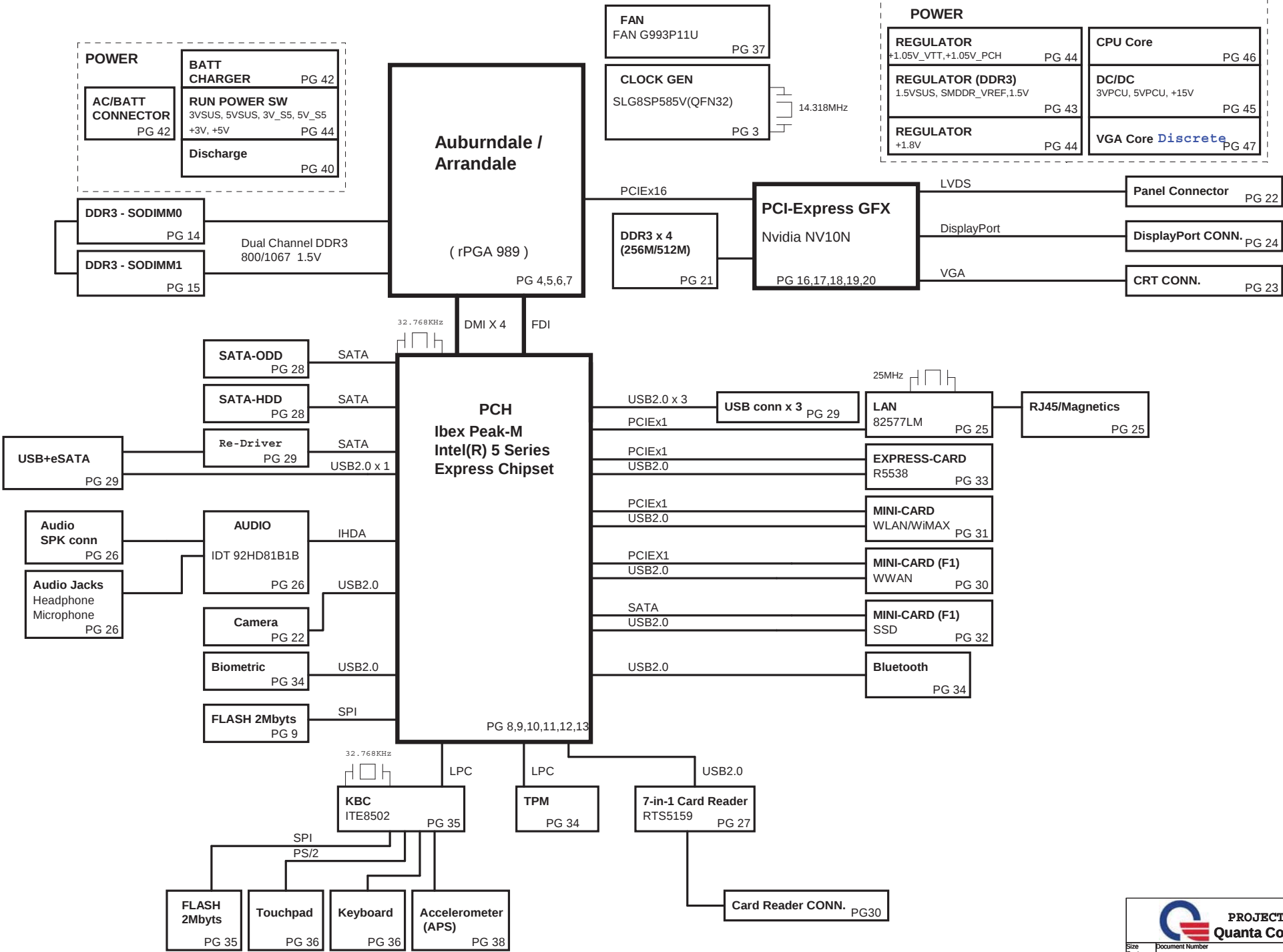


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Power States

POWER PLANE	VOLTAGE	PAGE	DESCRIPTION	CONTROL SIGNAL	ACTIVE IN
VIN	10V~+20V	23,32,43,44,45,46,47,48,49,50	MAIN POWER		S0-S5
+3VRTC	+3.0V~+3.3V	9,12,41	RTC		S0-S5
3VPCU	+3.3V	9,23,27,30,32,35,39,41,43,44,47	ITE8052 POWER	3V5V_EN	S0-S5
5VPCU	+5V	14,43,44,45,46,47,49,50	DC/DC POWER IC SOURCE	3V5V_EN	S0-S5
+15V	+15V	23,38,43,45,46,47	LARGE POWER	3V5V_EN	S0-S5
LANVCC	+3.3V	27,43	LAN POWER	LAN_ON	
5V_S5	+5V	12,29,30,43	PCH SUS POWER	S5_ON	S0-S3
3V_S5	+3.3V	8,9,10,11,12,43,52	Sys Management,PCH Resume Well, Intel HD Audio,USB,WLAN WiMAX POWER	S5_ON	S0-S3
5VSUS	+5V	23,39,43,48	SLP_S4# CTRLD POWER	SUSON	S0-S3
3VSUS	+3.3V	14,15,30,34,41,43,49	SLP_S4# CTRLD POWER	SUSON	S0-S3
1.5VSUS	+1.5V	4,6,14,15,43,45,46,49,50	SODIMM POWER	SUSON	S0-S3
0.75VSMDDR_VTERM	+0.75V	14,15,43,45	DDR3 SODIMM REFERENCE POWER	MAIN_ON	S0
+5V	+5V	12,18,23,24,25,26,28,35,37,41,43,44	SLP_S3# CTRLD POWER	MAIN_ON	S0
+3V	+3.3V	3,4,8,9,10,11,12,14,15,17,23,25,26,27,28,29,30,31,32,33,34,36,37,38,39,40,41,43,44,45,46,47,48,50,52	SLP_S3# CTRLD POWER	MAIN_ON	S0
+1.8V	+1.8V	6,12,17,18,21,22,33,43,50	LVDS,NVM POWER	MAIN_ON	S0
+1.5V	+1.5V	12,18,19,20,31,32,34,45,46	Mini PCIe,Express Card POWER	MAIN_ON	S0
+1.05V_VTT	+1.05V	4,6,11,12,43,46,48,52	AuBurndale VTT POWER	MAIN_ON	S0
+1.05V_PCH	+1.05V	3,10,12,43,46,52	PCH CORE POWER	1.05V_RUN_ON	S0
+VCC_GFX_CORE	+0.9V~+1.2V	18,21,43,49	VGA CORE POWER	GFXVR_EN	S0
VCC_CORE		6,43,48	CPU CORE POWER	VRON	S0
LCDVCC	+3.3V	23	LCD Power	ENVDD	S0
+5V_ODD	+5V	28	ODD Power	MAIN_ON	S0
+5V_HDD	+5V	28	HDD Power	MAIN_ON	S0
BAT-V	+10V~+17V	44	MAIN BATTERY	CHG_PBATT	S0-S5



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Size
A3

Document Number

FRONTPAGE

Rev
1E

Date:

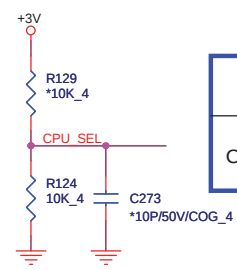
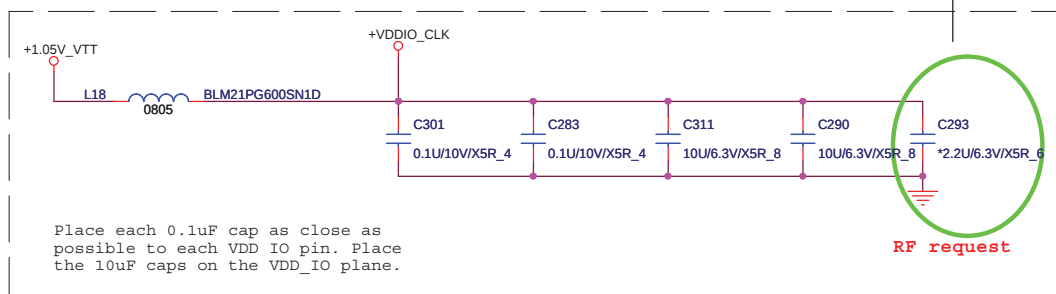
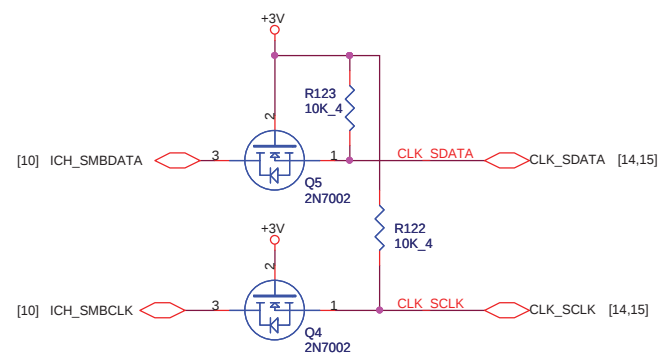
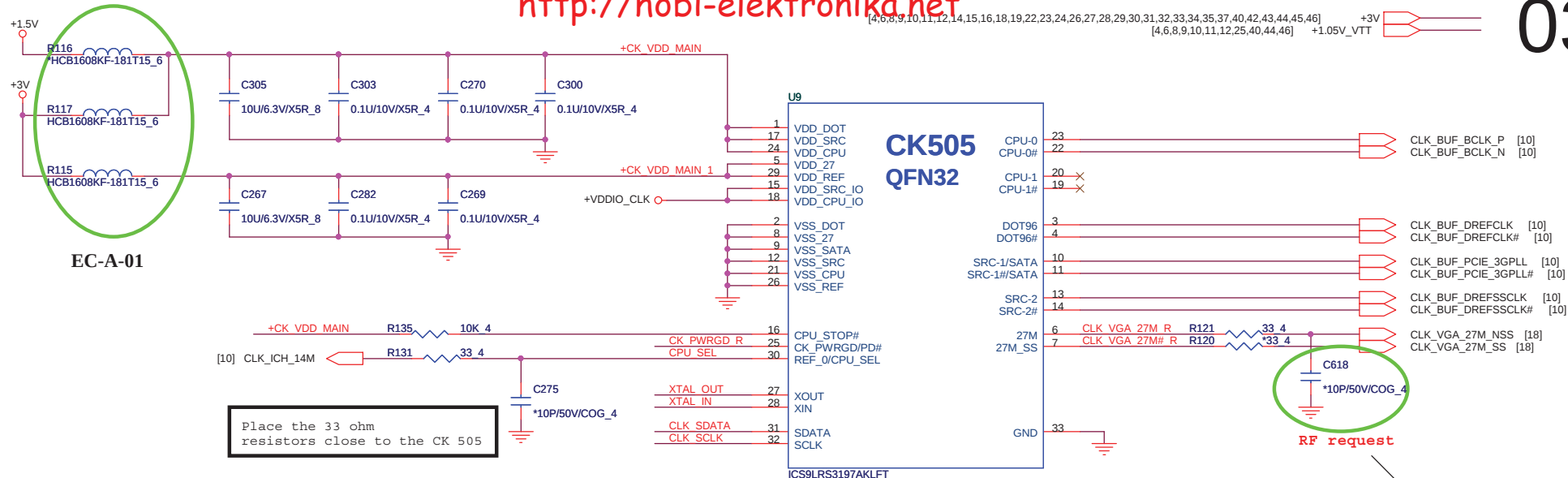
Tuesday, January 26, 2010

Sheet

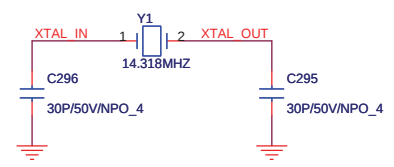
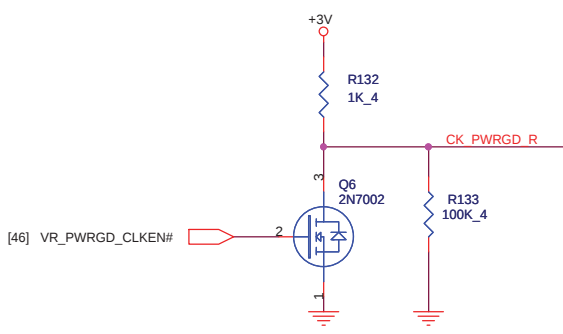
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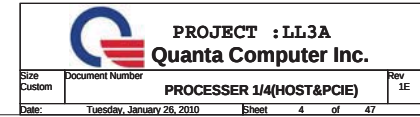
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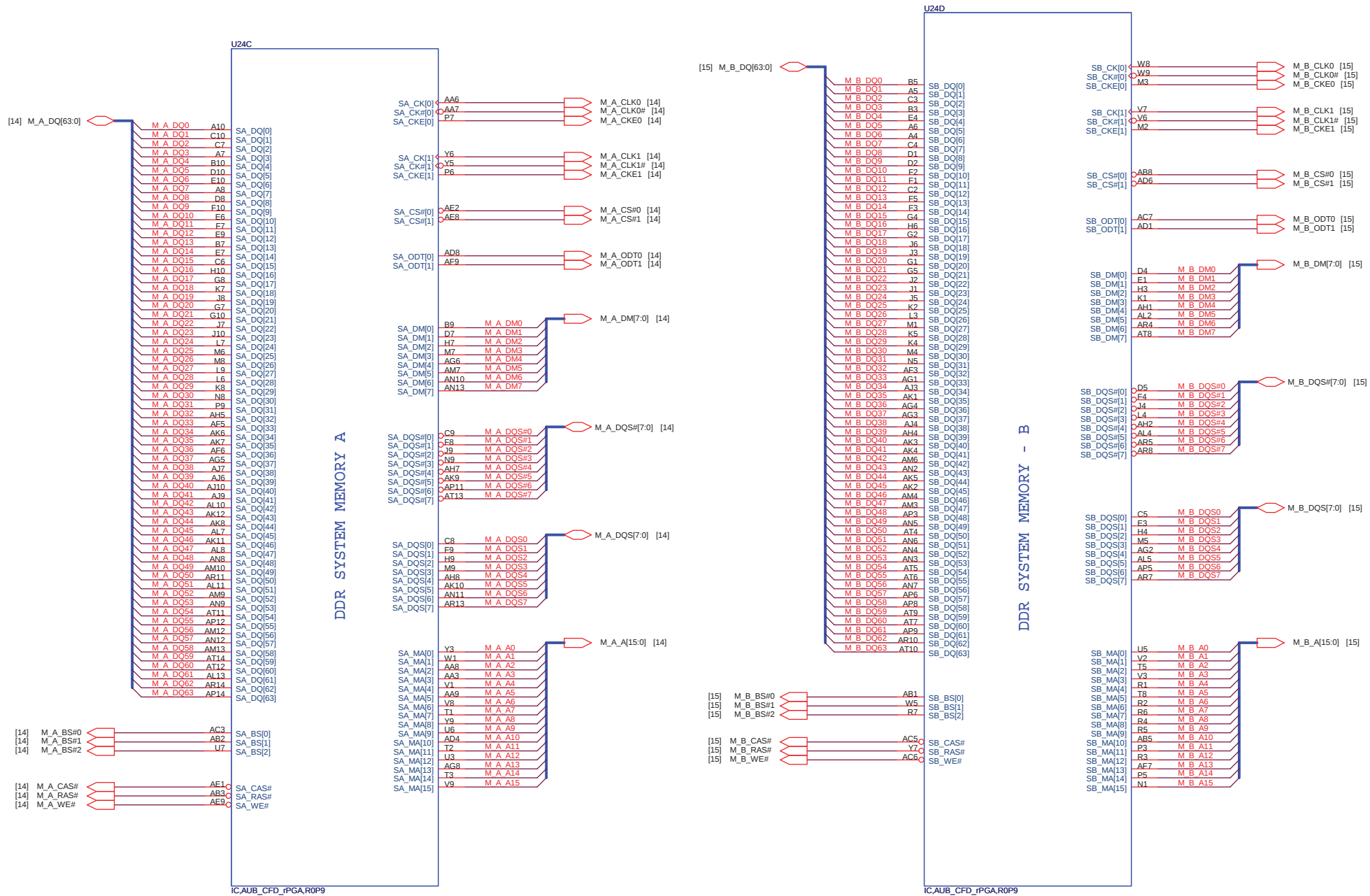
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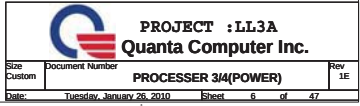


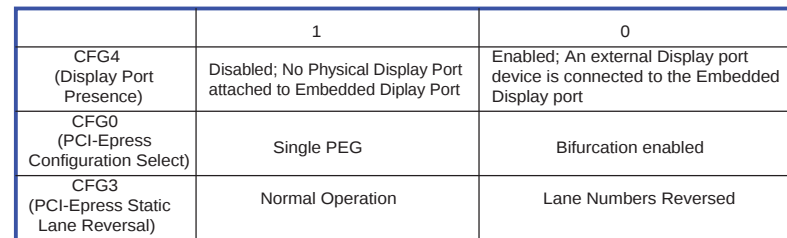
	0	1
CPU_SEL	CPU0/1=133MHZ (default)	CPU0/1=100MHZ





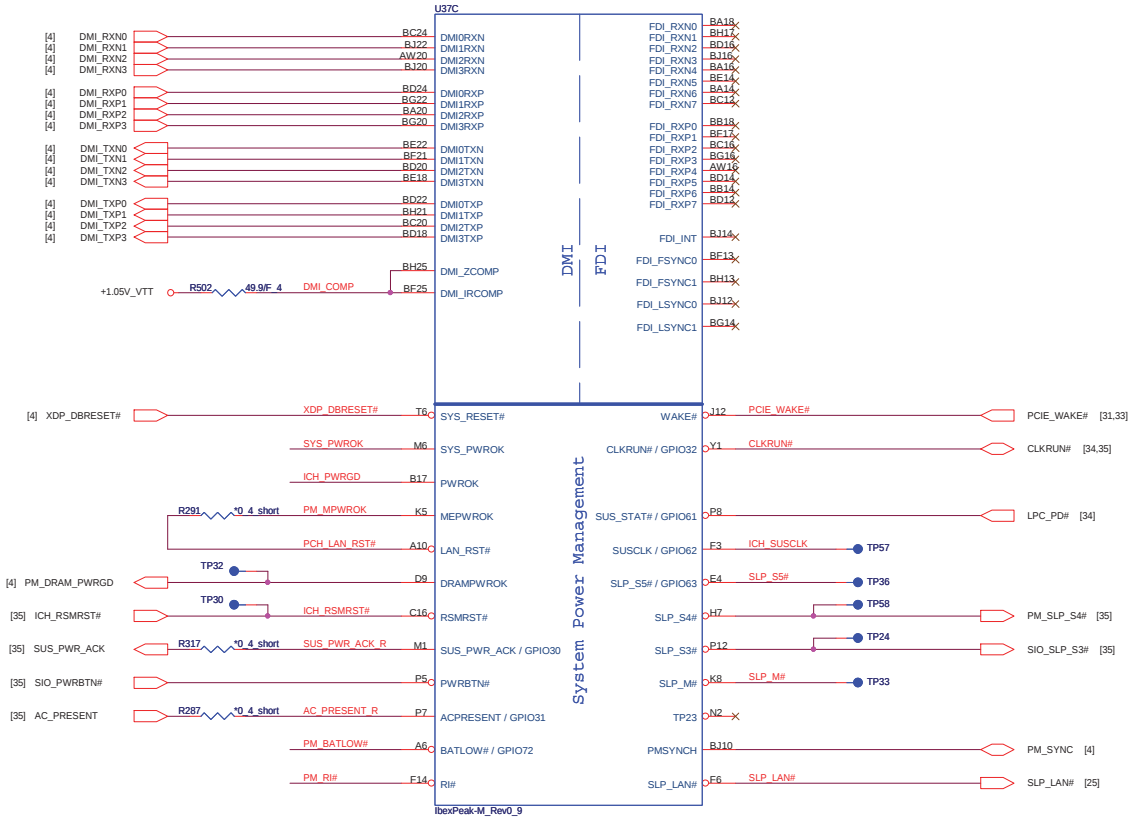




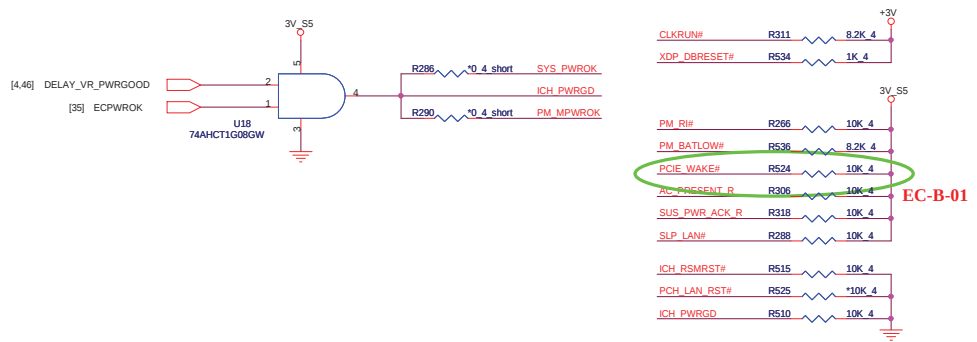
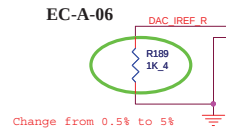
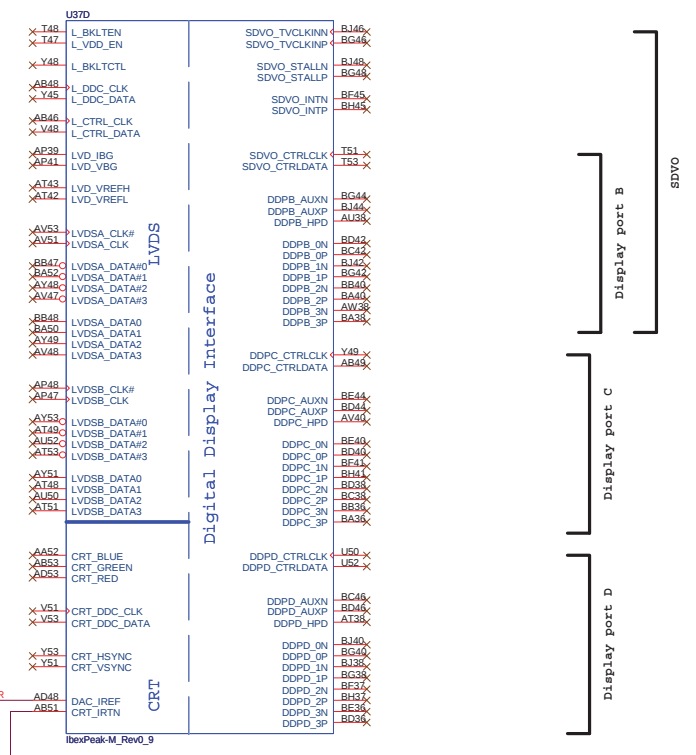


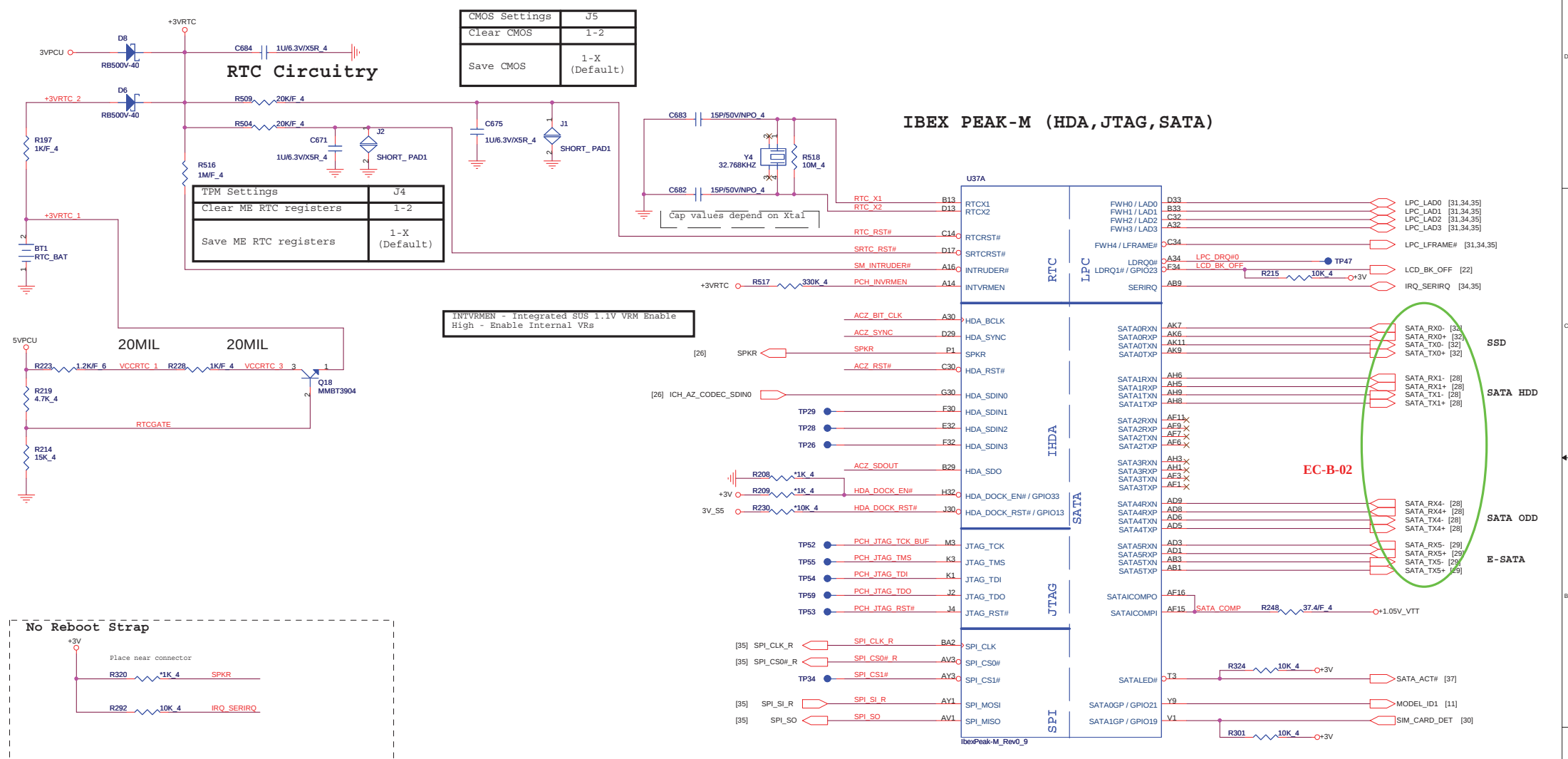
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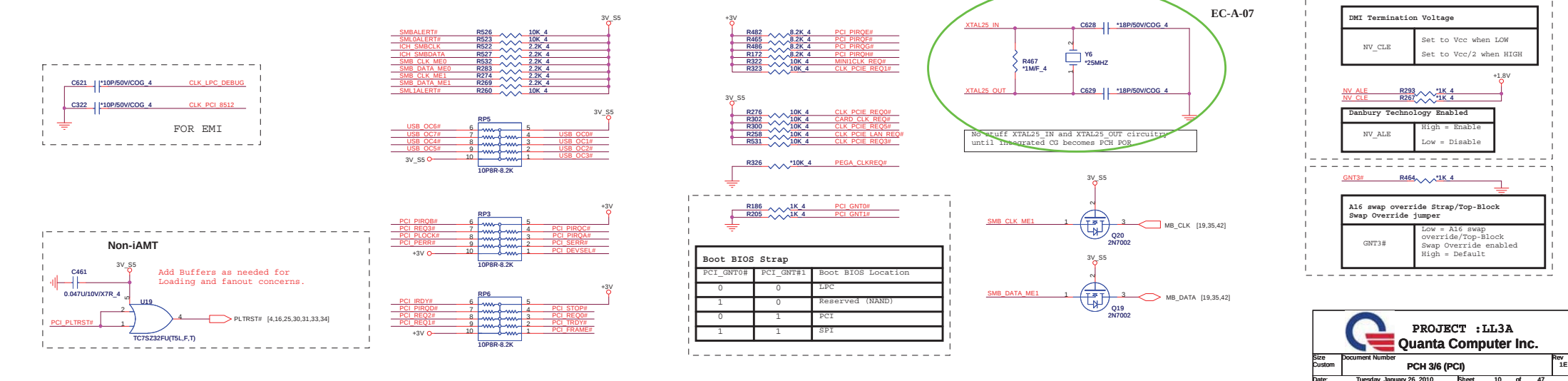
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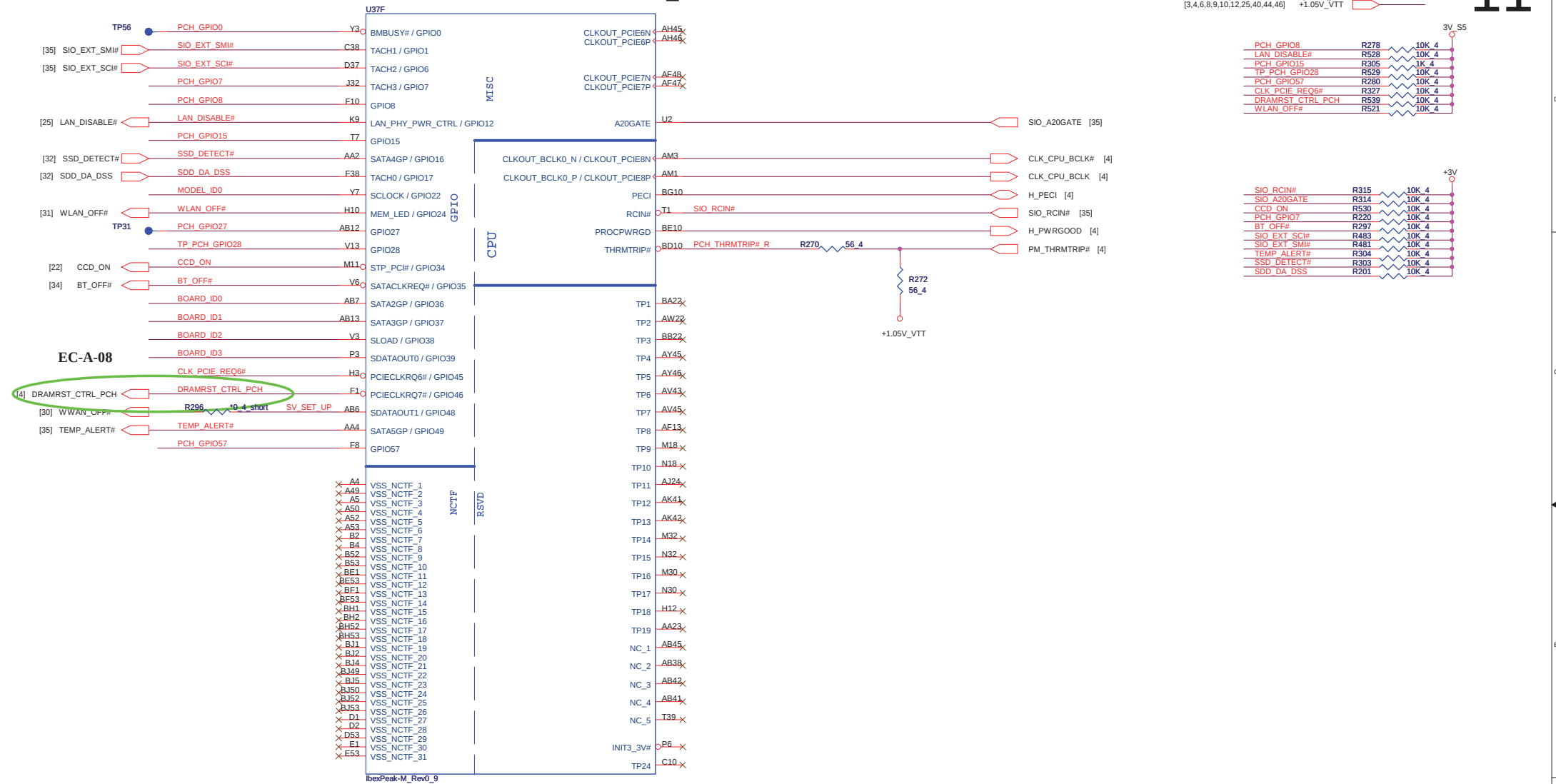


IBEX PEAK-M (LVDS, DDI)



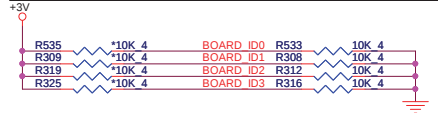






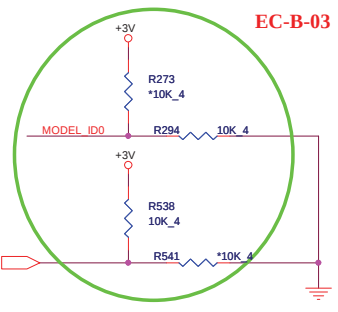
Board ID

Board ID For Function	ID3 GPIO39	ID2 GPIO38	ID1 GPIO37	ID0 GPIO36
SDV	0	0	0	0
SIV	0	0	0	1
SIT	0	0	1	0
SVT	0	0	1	1
SOVP	0	1	0	0



Model ID

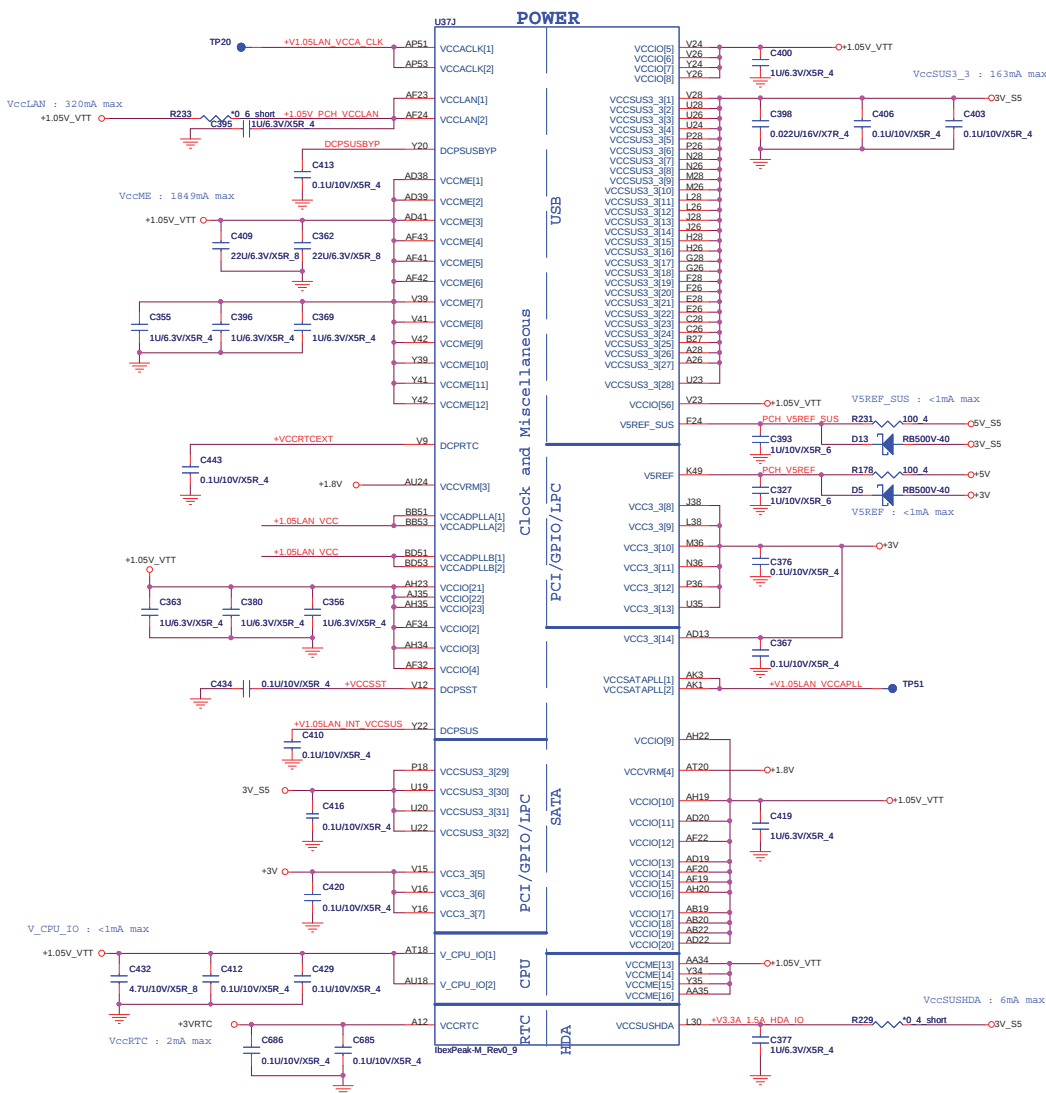
Model ID	MODEL_ID0	MODEL_ID1
Default	0	0
LL3/LL3A	0	1
LL5/LL5A	1	0
	1	1

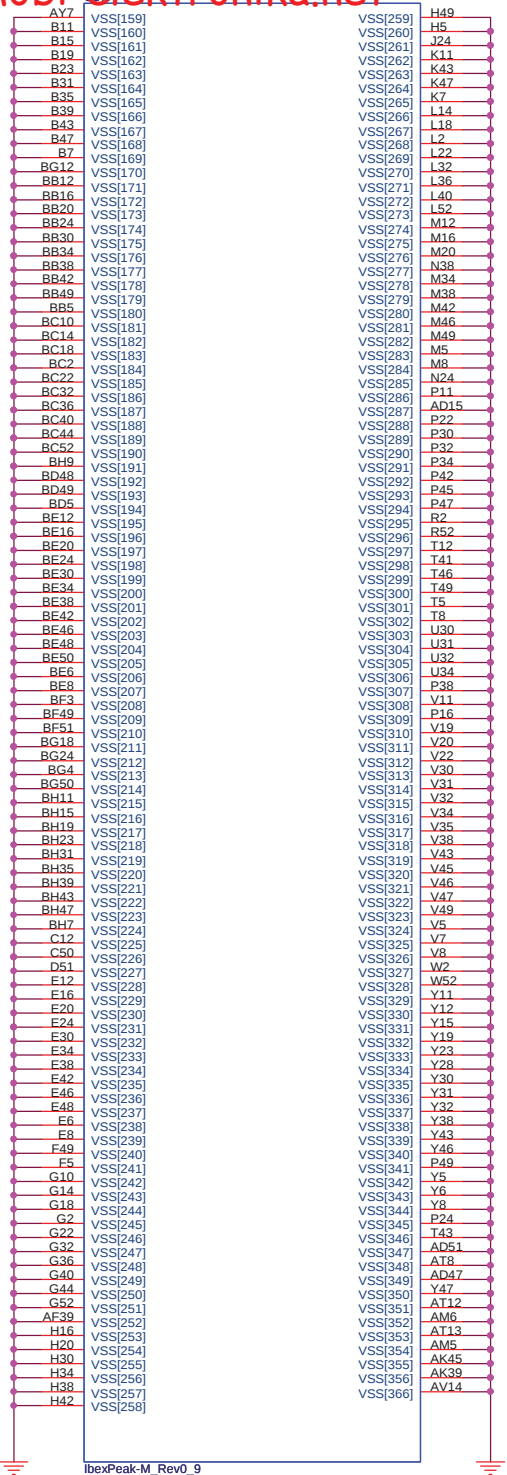
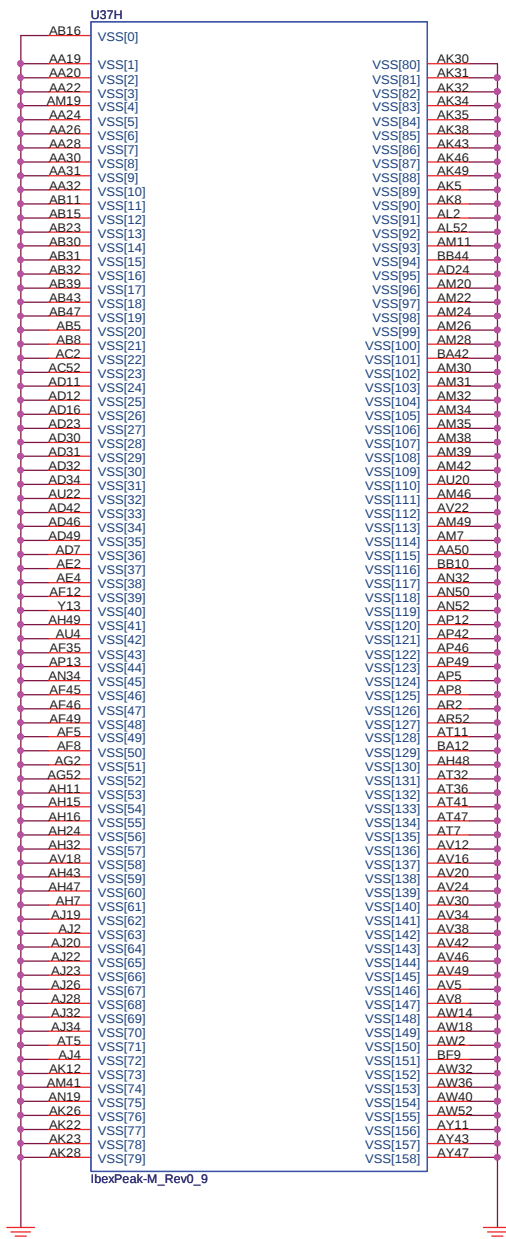


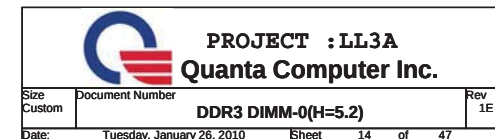
SV_SET_UP 1-X High = Strong (Default)

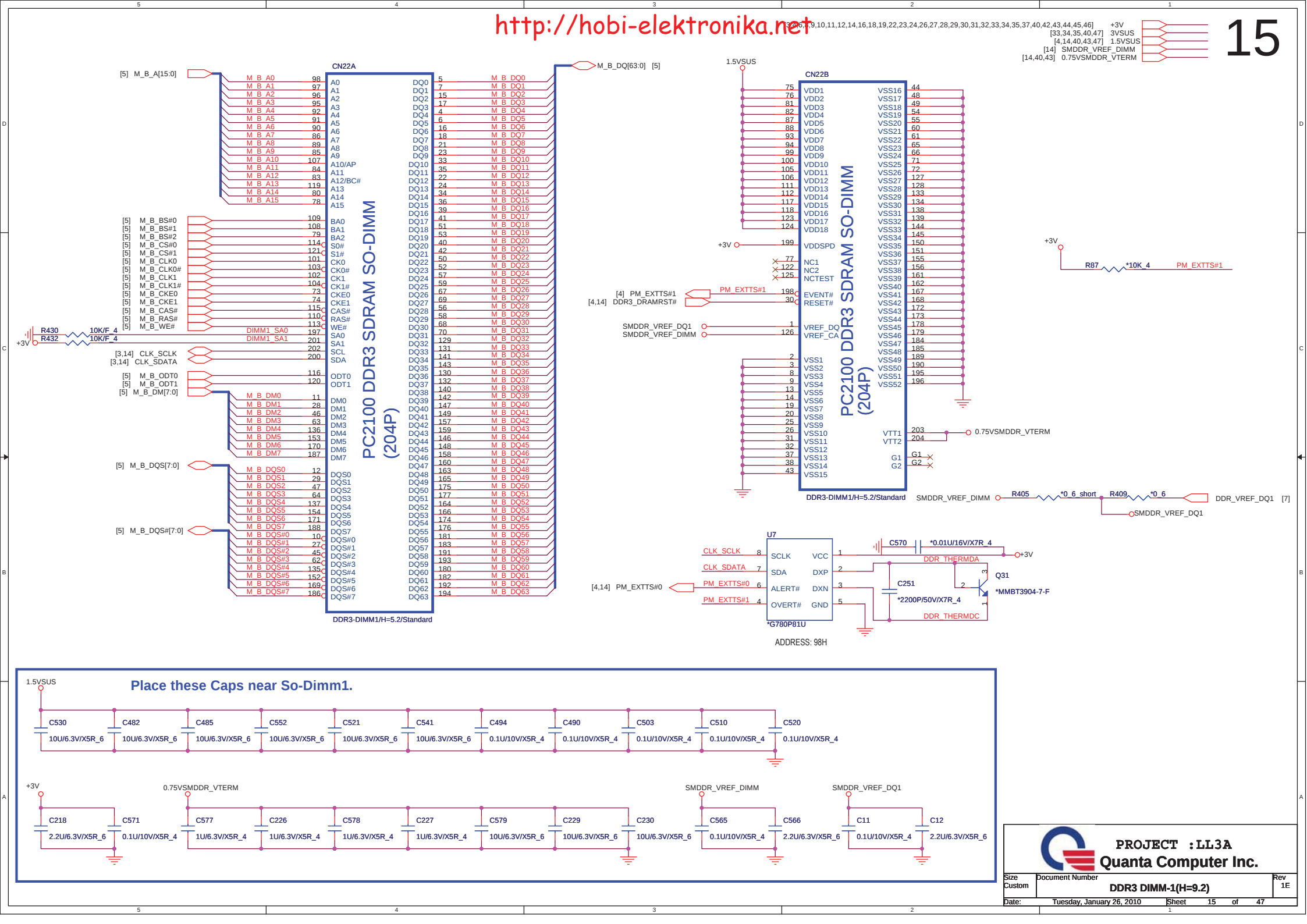
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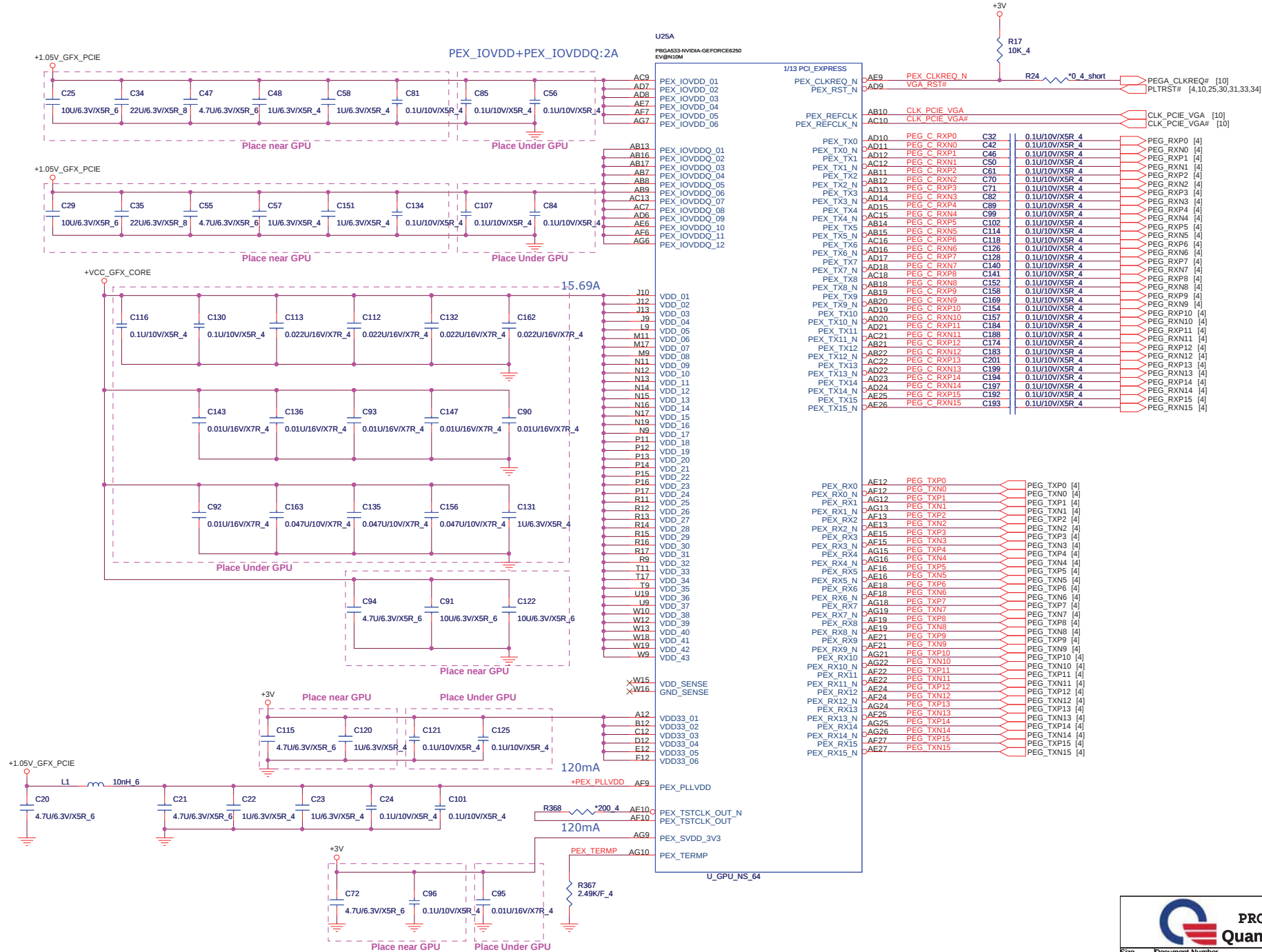
Size Custom	Document Number	Rev 1E
PCH 4/6 (GPIO)		
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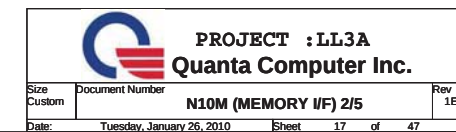


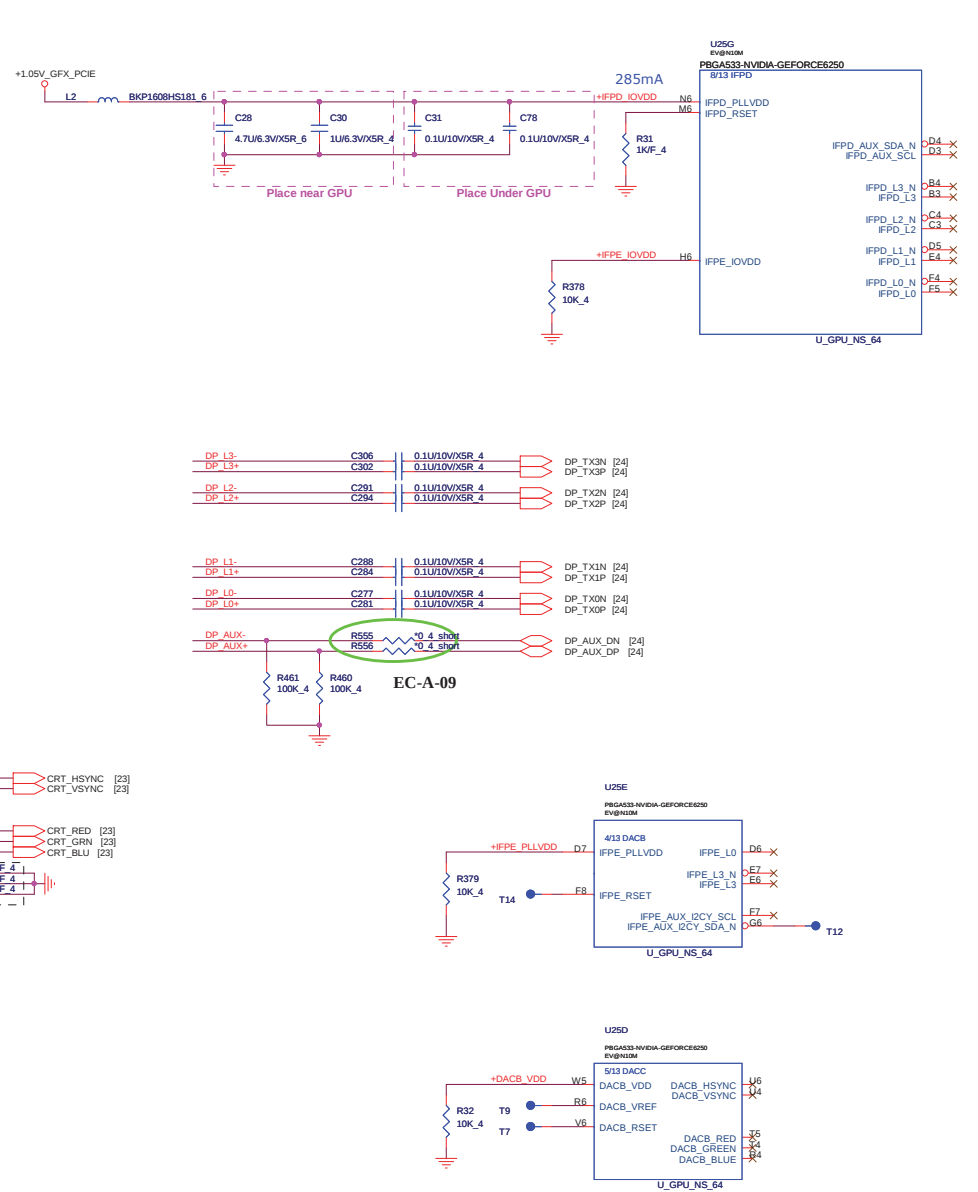


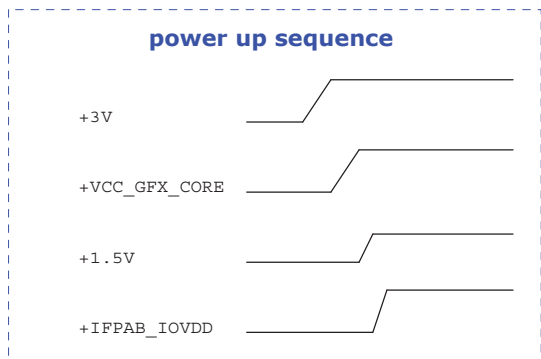
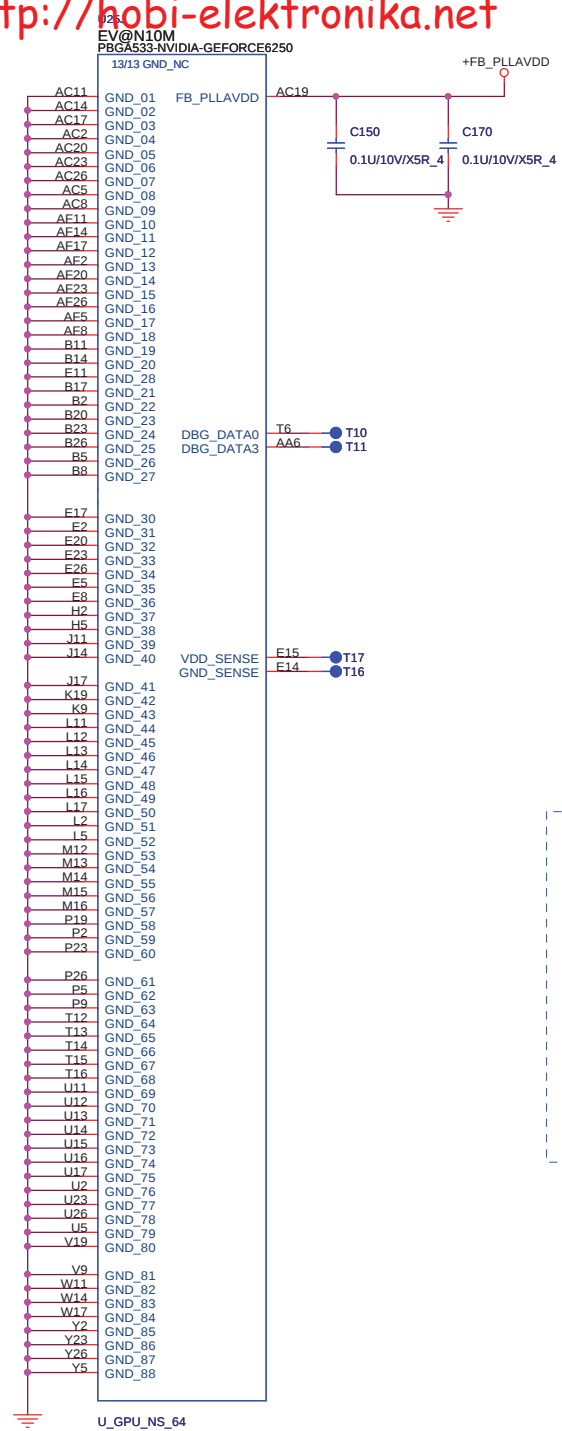


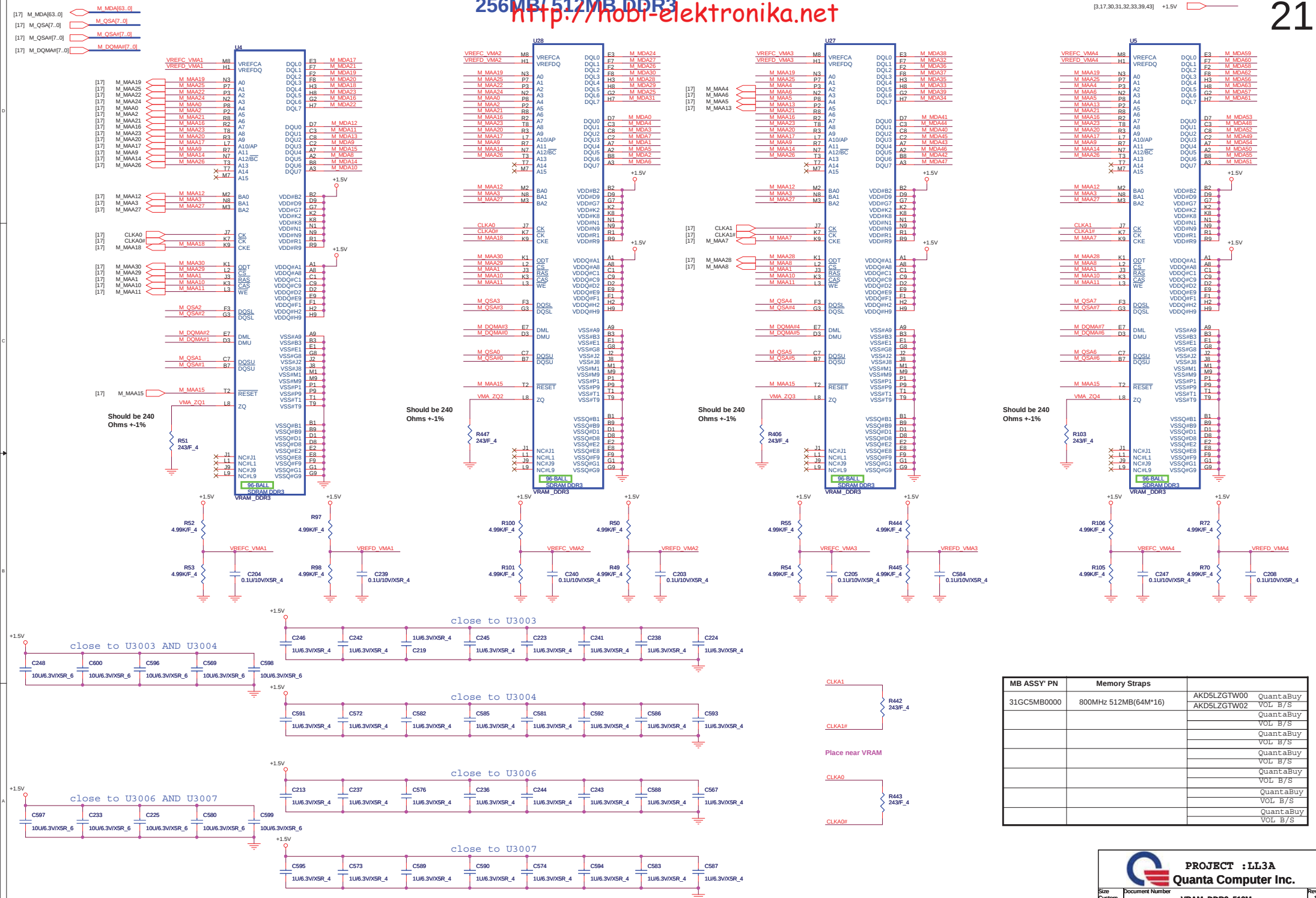


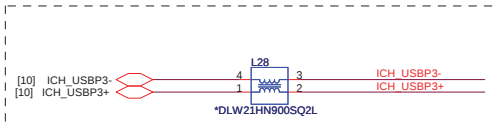
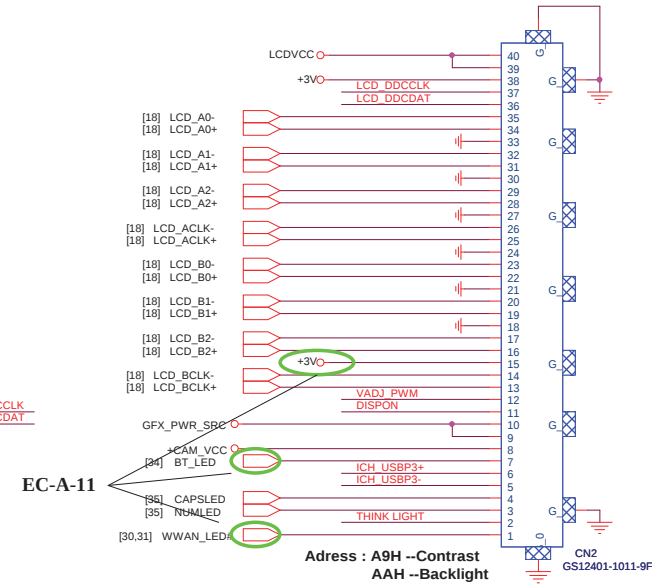
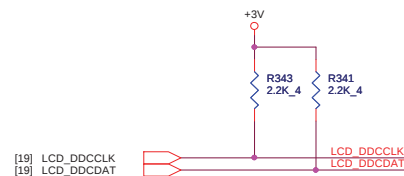






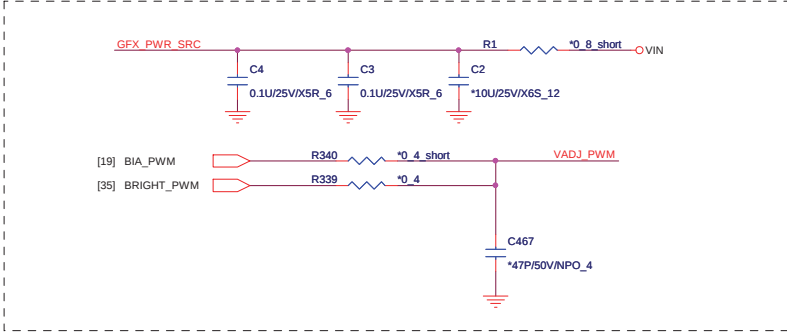


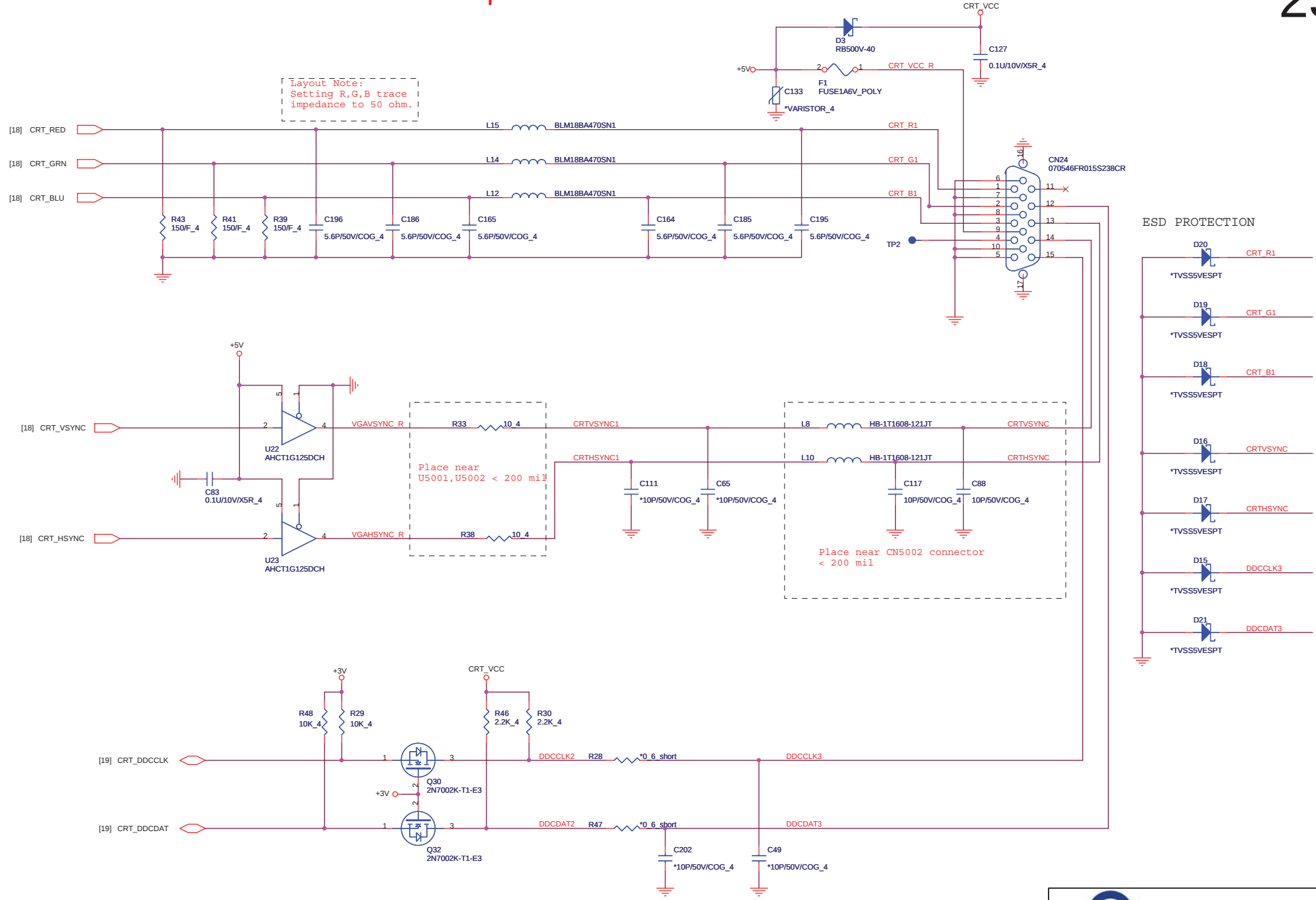
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CAMERA VCC Control

The schematic diagram illustrates the CAMERA VCC Control circuit. It features a +5V supply, a +CAM_VCC supply, and a ground. A CCD_ON signal is connected to the SHDN pin of the AT5231H-3.3KER (U1) via a 10k resistor (R9). The VIN pin of U1 is connected to the +5V supply via a 1uF capacitor (C14) and to the +CAM_VCC supply via a 0.4 ohm resistor (R10). The VOUT pin of U1 is connected to the +CAM_VCC supply via a 0.8 ohm resistor (R11). The GND pin of U1 is connected to ground. The SET pin of U1 is connected to ground via a 100k resistor (R3). The output of the circuit is connected to the +CAM_VCC supply via a 215k resistor (R5) and a 4.7uF capacitor (C13).



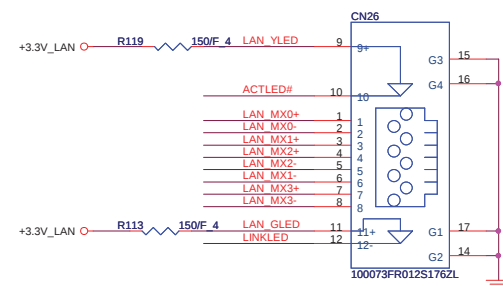
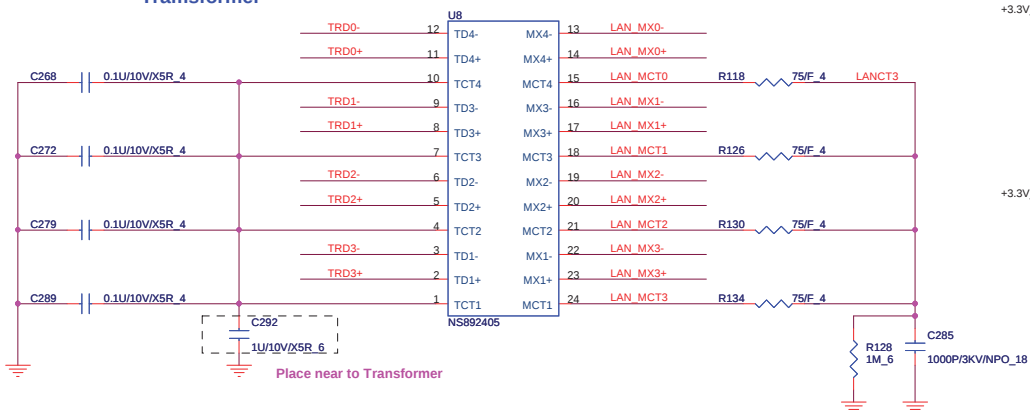
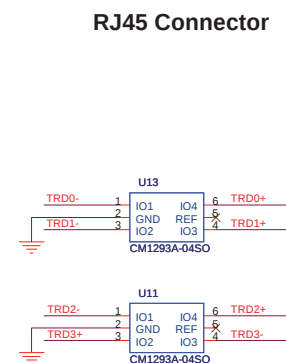
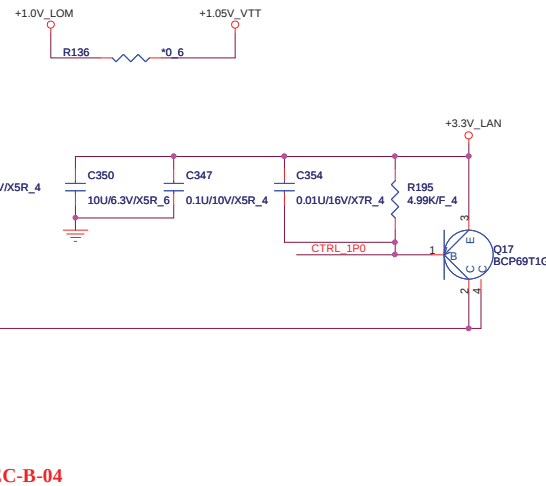
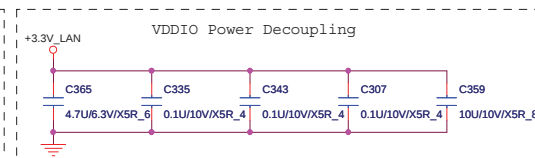
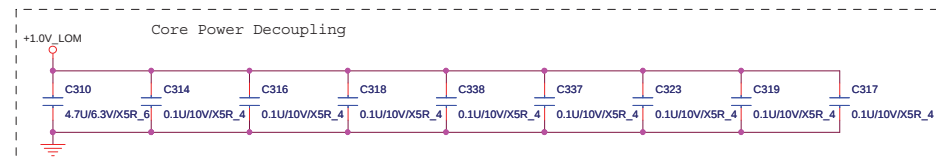


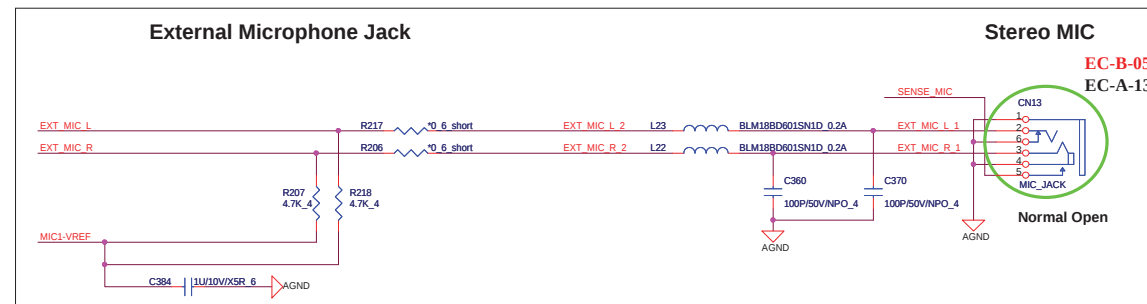


ESD Protect

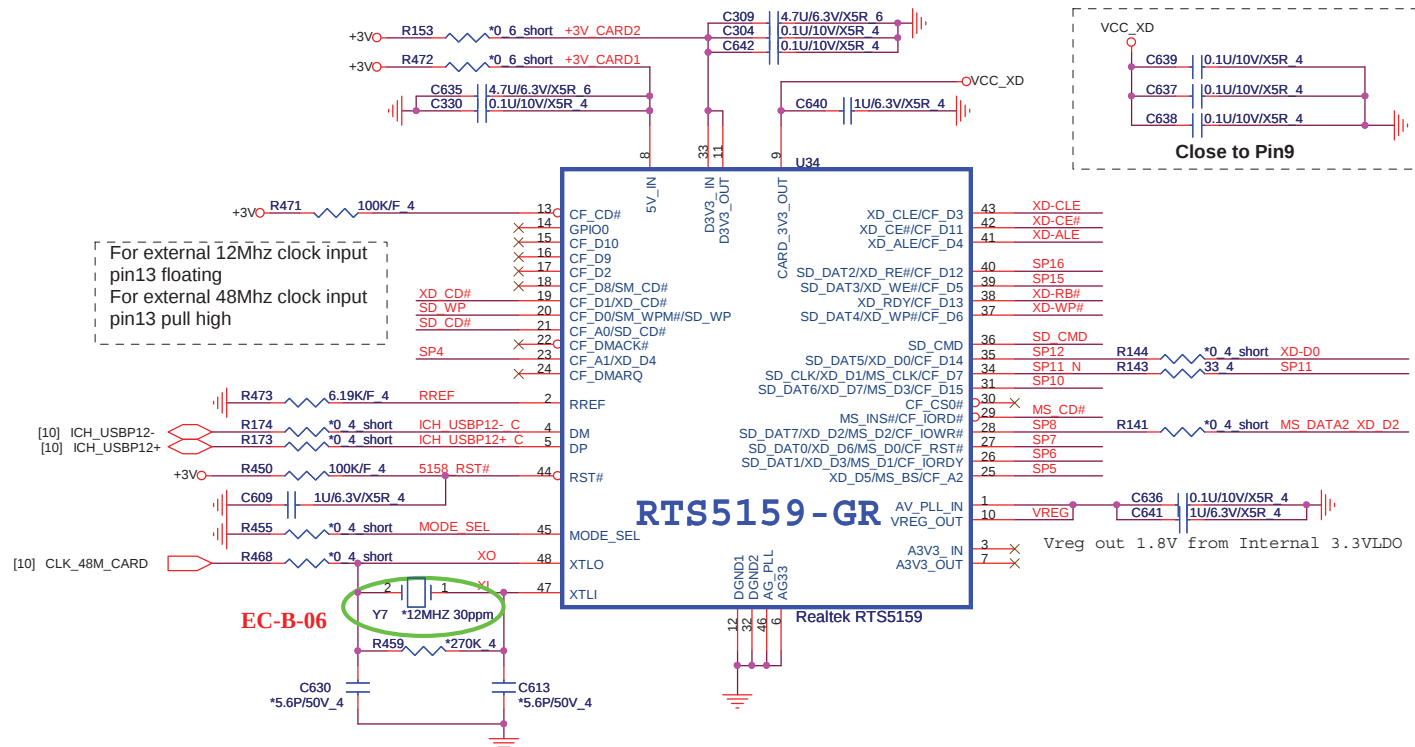


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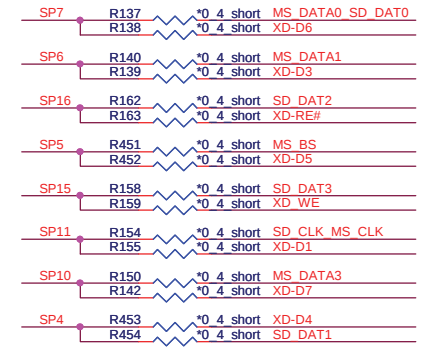
Sense_A	Sense_B	RES
Port_A	Port_E	39.2K
Port_B	Port_F	20K



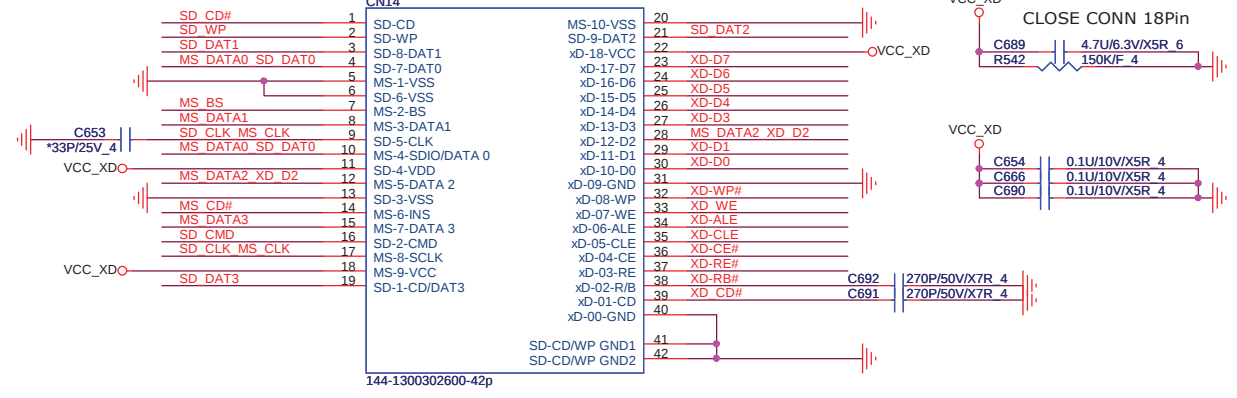
Note:

SD/MMC	MS	XD
SP0		XD_CD#
SP1	SD WP	XD_CD#
SP2	SD CD#	
SP3	SD DAT1	XD D4
SP4	MS BS	XD D5
SP5	MS D1	XD D3
SP6	MS D0	XD D6
SP7	SD DAT0	XD D2
SP8	SD DAT7	XD D2
SP9	MS INS#	
SP10	SD DAT6	XD D7
SP11	SD CLK	MS SCLK
SP12	SD DAT5	XD D0
SP13	SD DAT4	XD WP#
SP14		XD R/B#
SP15	SD DAT3	XD WE#
SP16	SD DAT2	XD RE#
SP17		XD ALE
SP18		XD CE#
SP19		XD CLE

For RTS5159



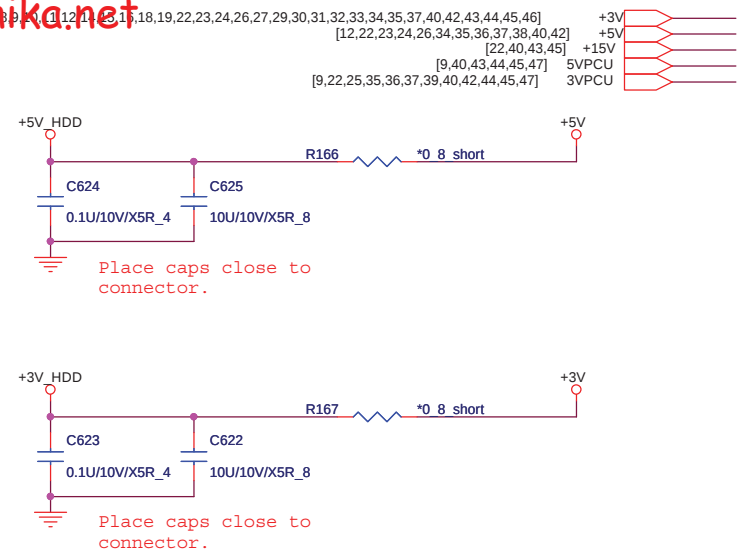
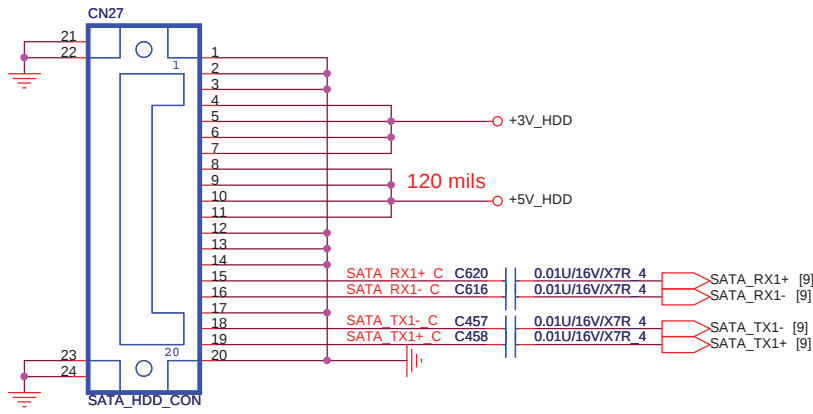
**5 IN1 CARD READER
XD, MMC/SD, MS/MSP**



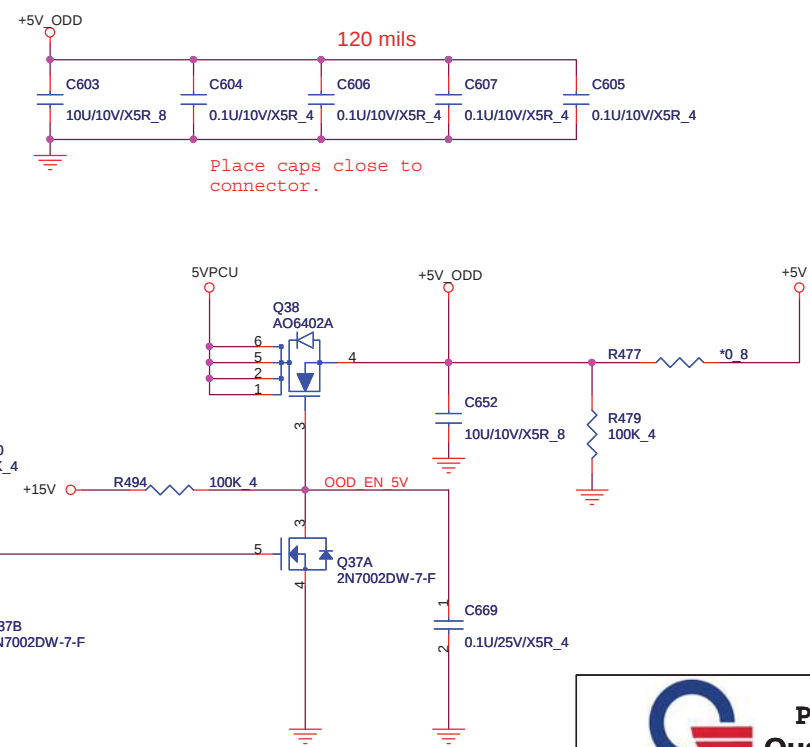
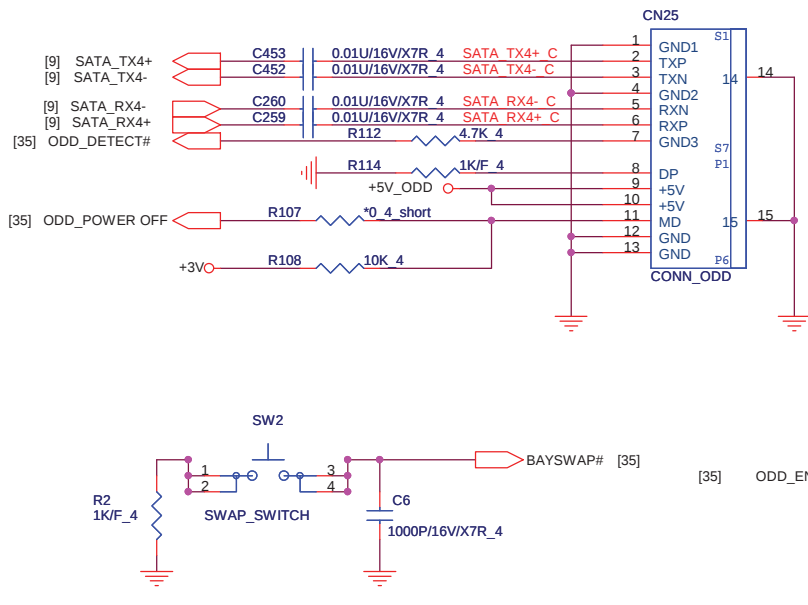
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Size B	Document Number	Card Reader (RTS5159)	Rev 1E
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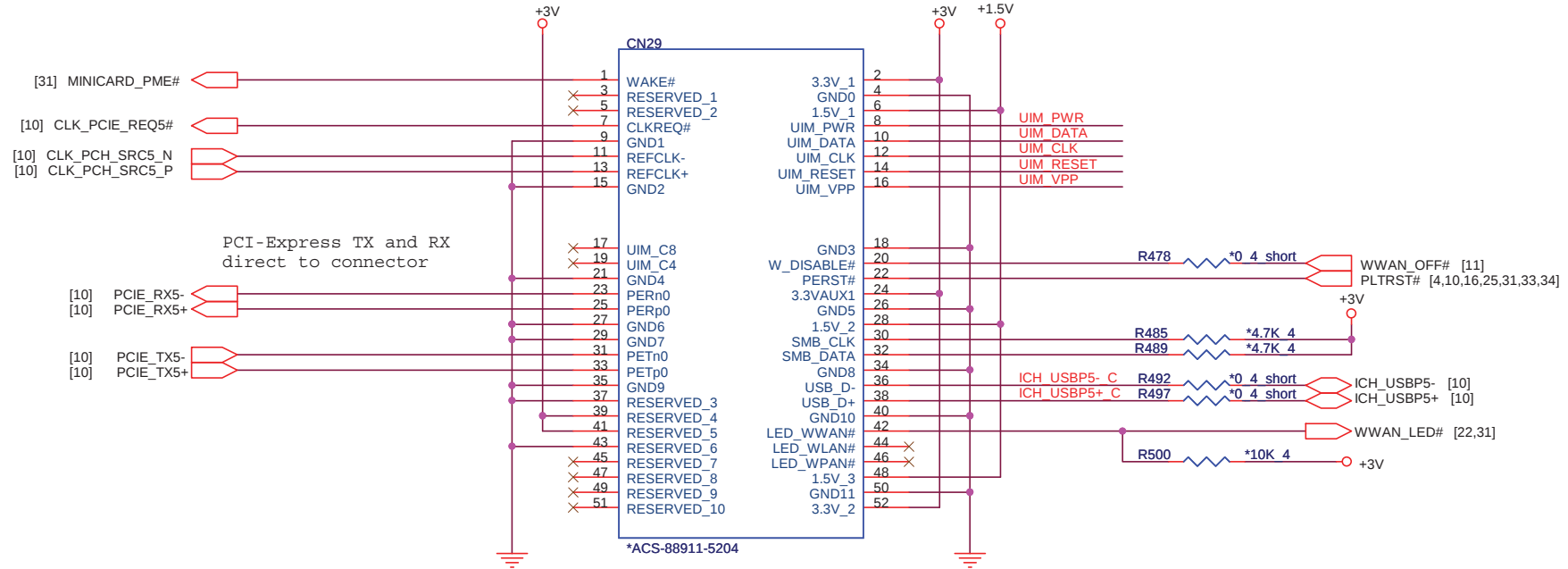
SATA-HDD CONNECTOR



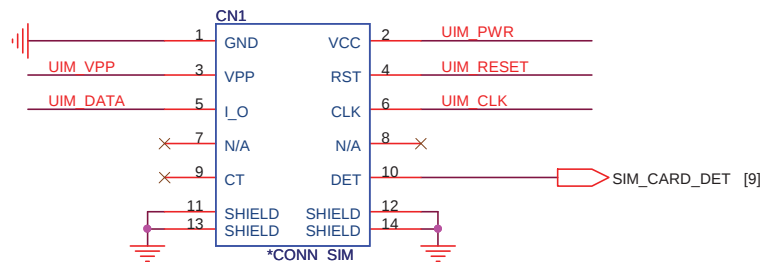
SATA CD-ROM



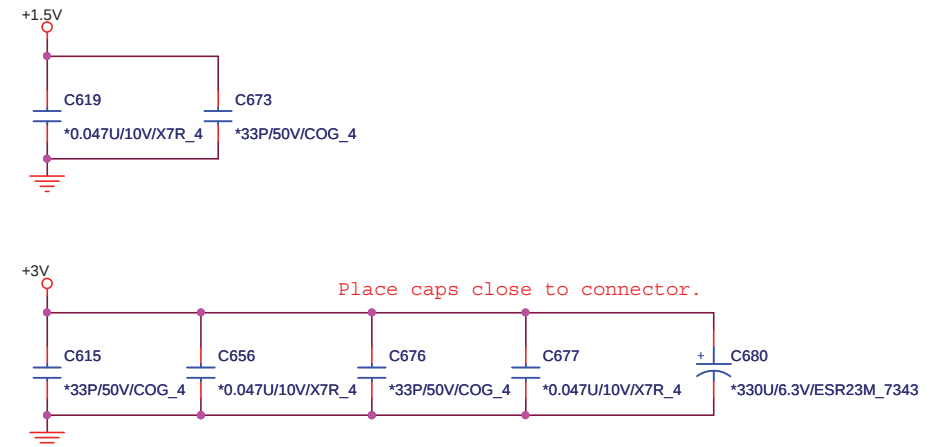
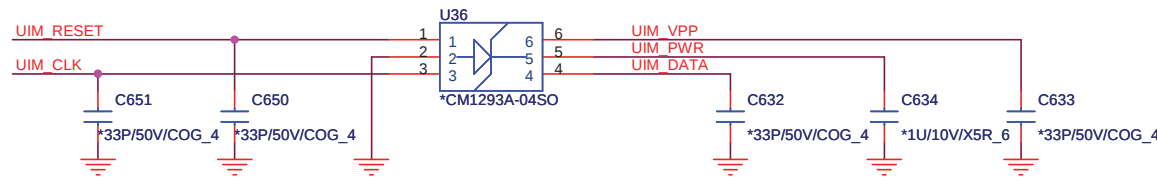
MiniCard WWAN connector



SIM Card CONN

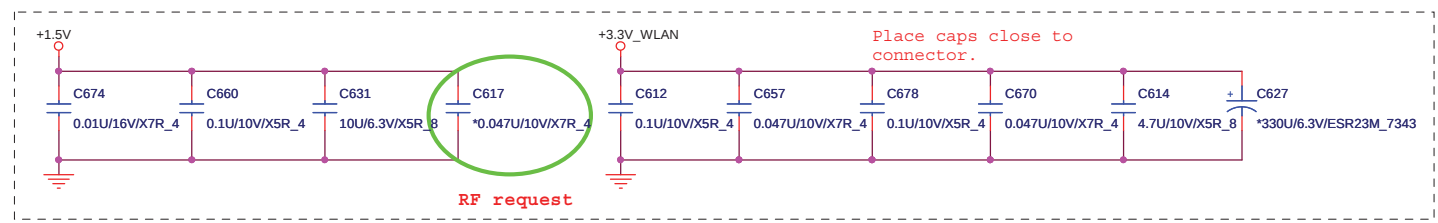
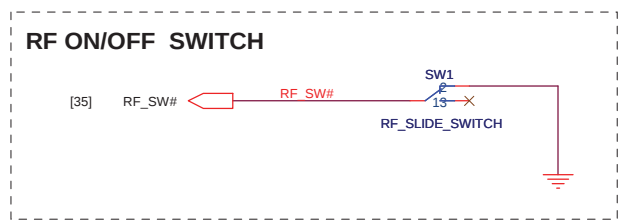
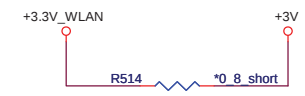
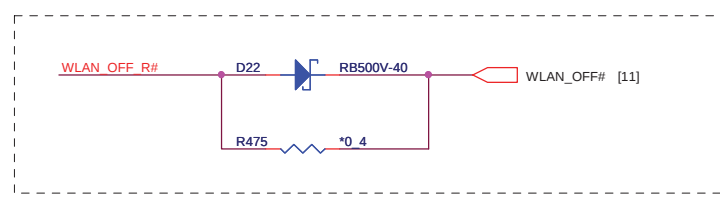
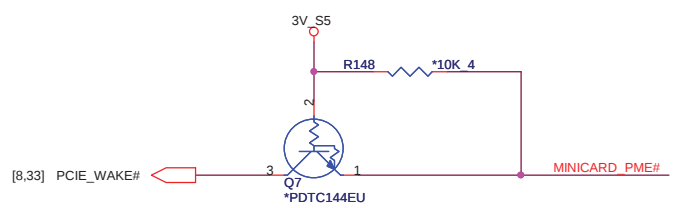
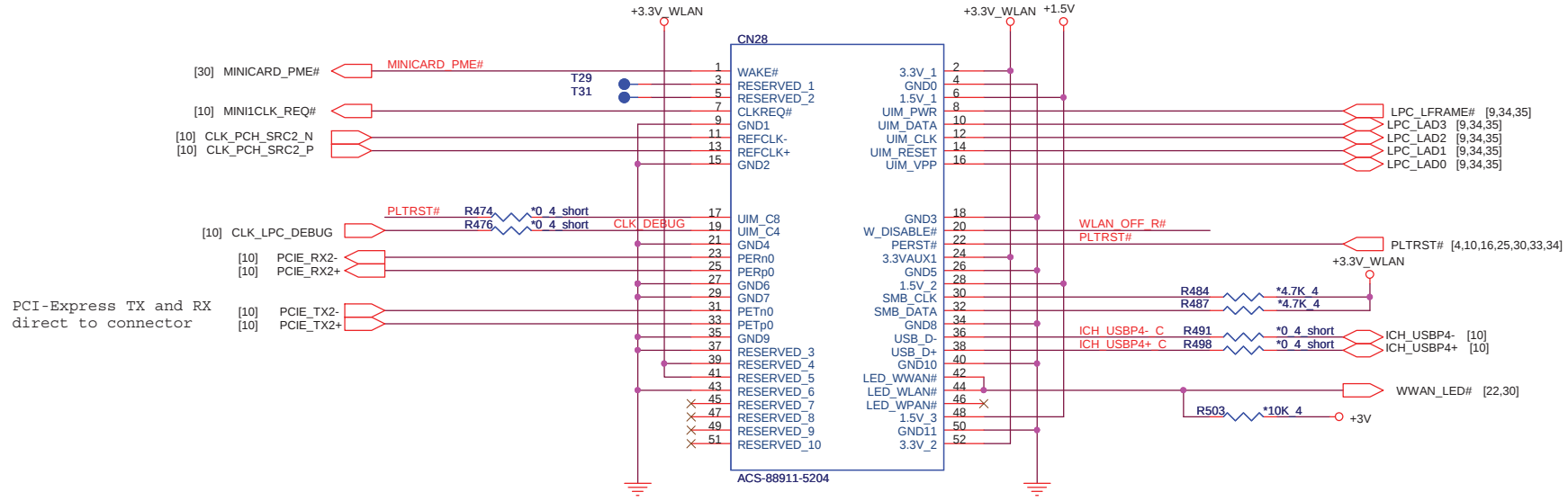


Layout Note:
UIM_RESET, UIM_CLK, UIM_DATA routing as short as possible



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Size Custom	Document Number	Rev 1E
	MINI-Card (WWAN)	
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EC-A-16

Mini PCI-E Card 3 SSD

<http://hobi-elektronika.net>

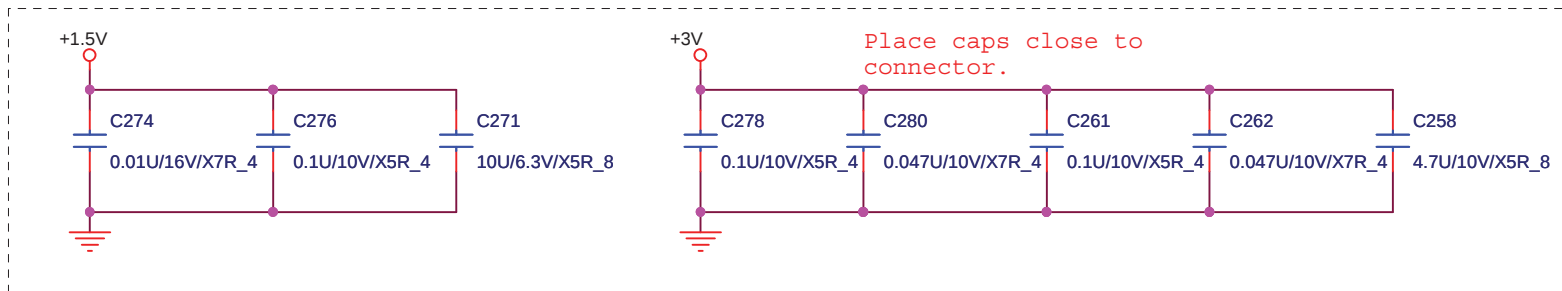
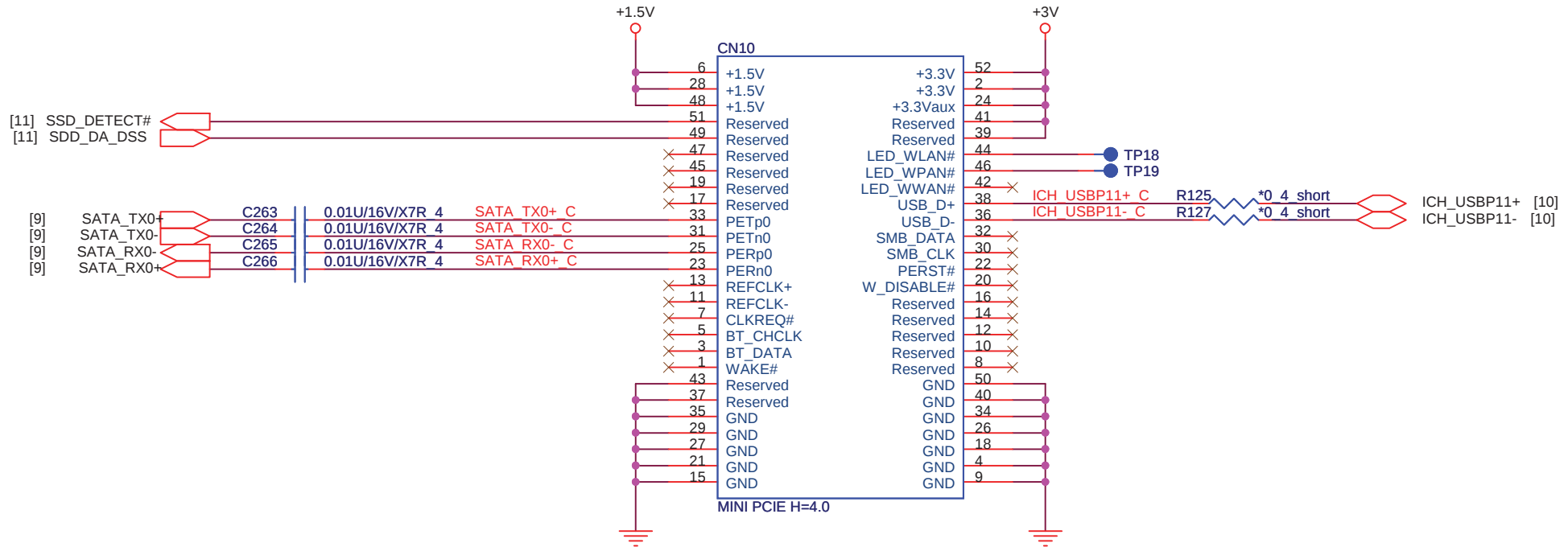
[3,17,21,30,31,33,39,43]

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+1.5V

+3V

32



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Size Custom	Document Number MINI Card (SSD)	Rev 1E
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Express Card

<http://hobi-elektronika.net>

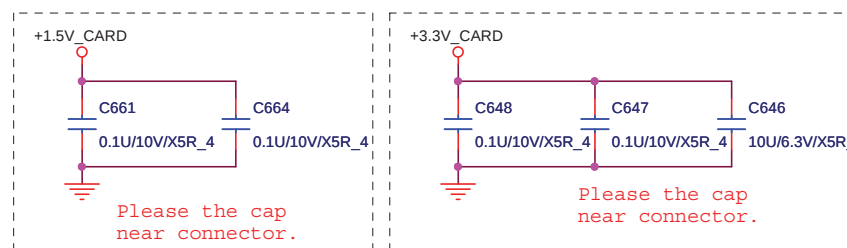
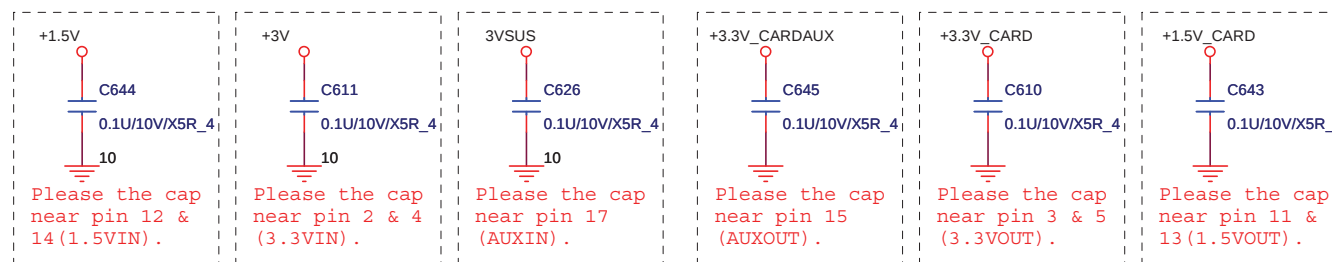
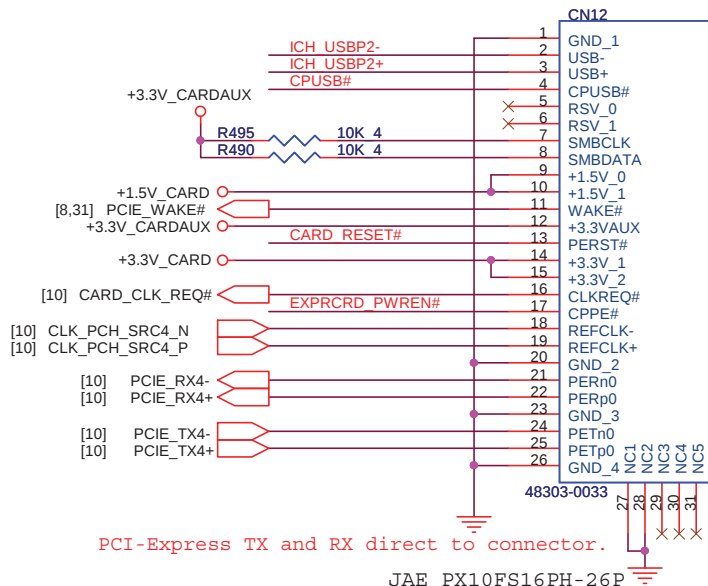
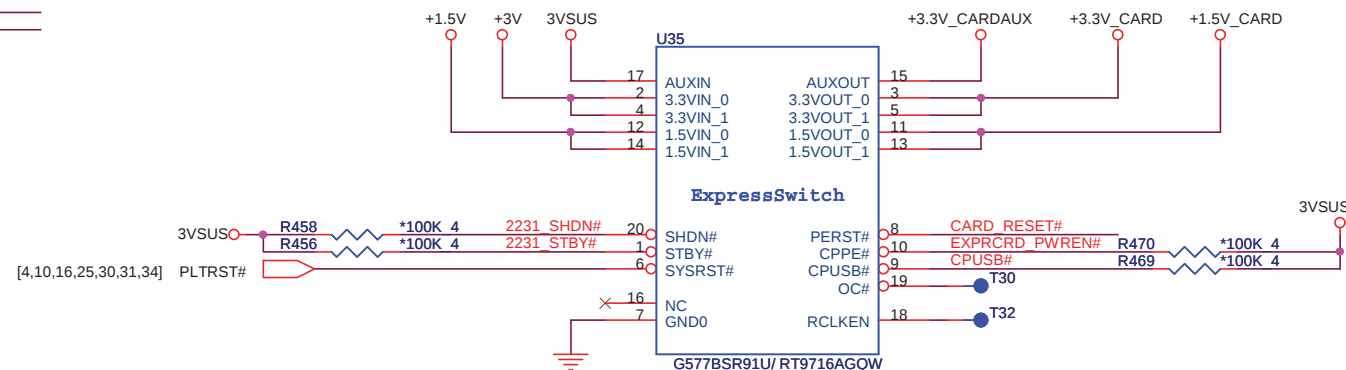
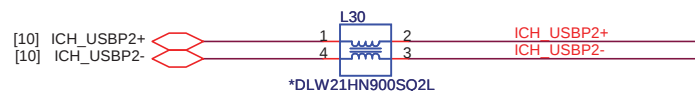
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[3,17,21,30,31,32,39,43]
[34,35,40,47]

+3V
+1.5V
3VSUS



33

+1.5V_CARD Max. 650mA, Average 500mA.
+3.3V_CARD Max. 1300mA, Average 1000mA.

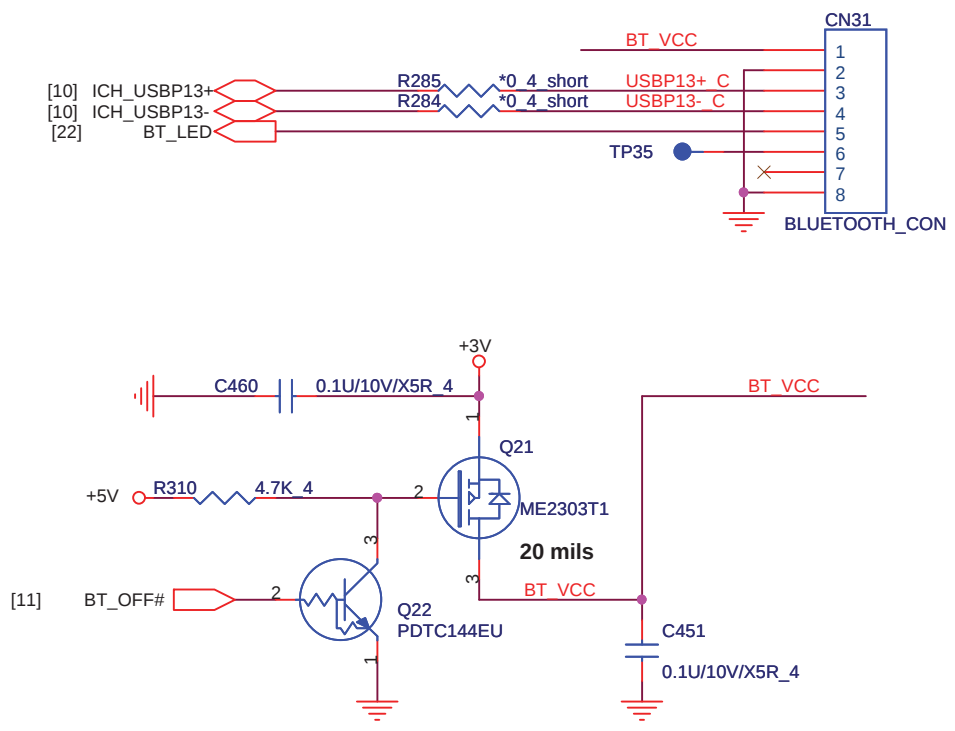




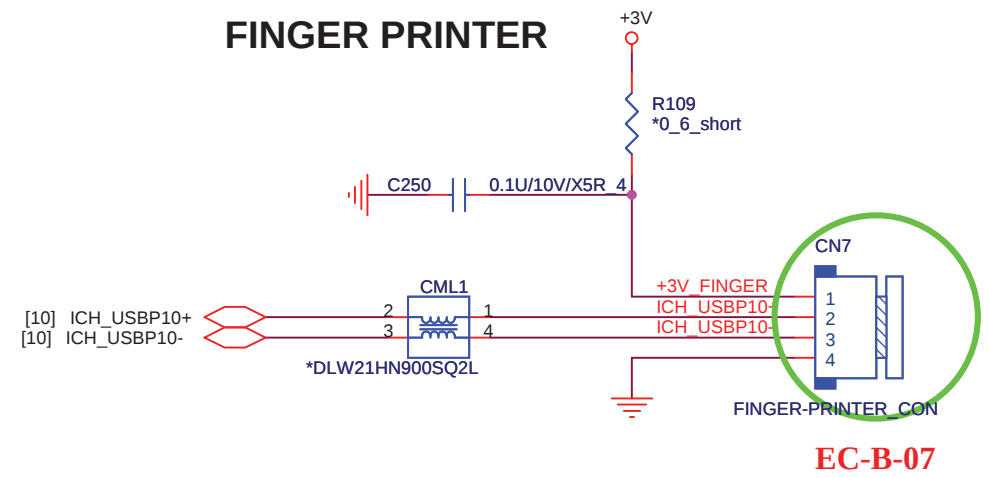
PROJECT : LL3A
Quanta Computer Inc.

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Express Card		
Date:	Tuesday, January 26, 2010	Sheet 33 of 47

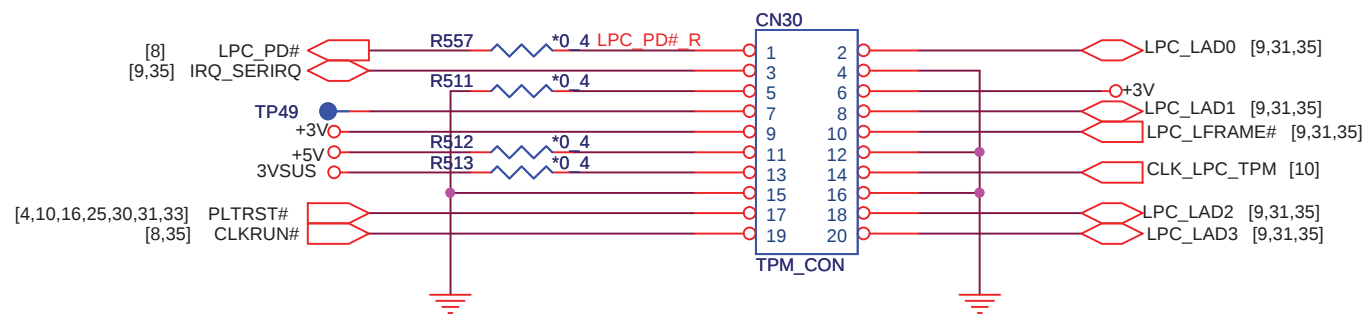
BLUETOOTH



FINGER PRINTER

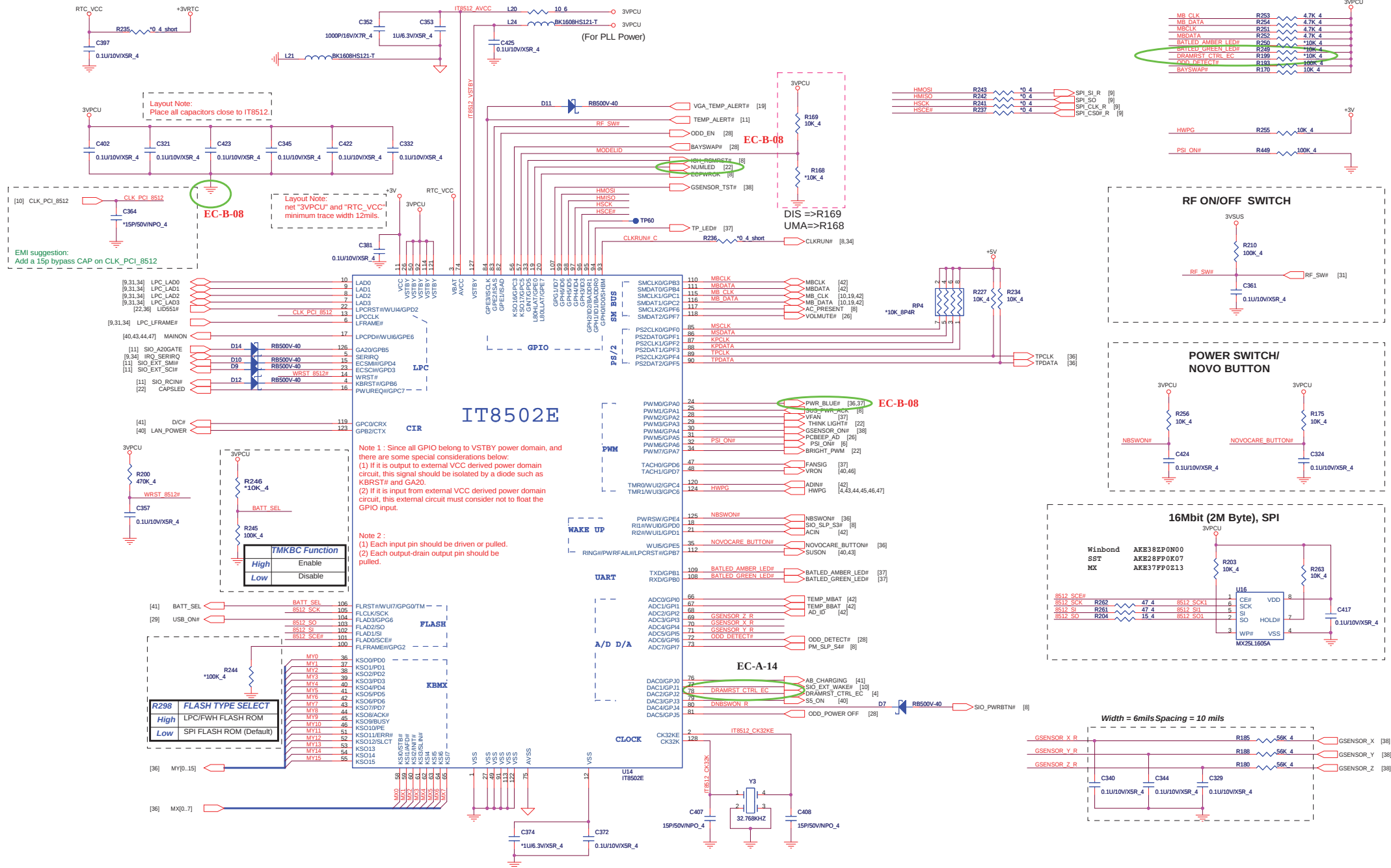


TPM

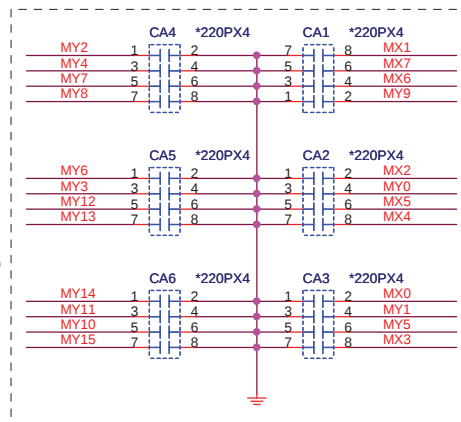
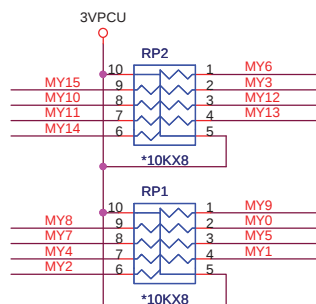
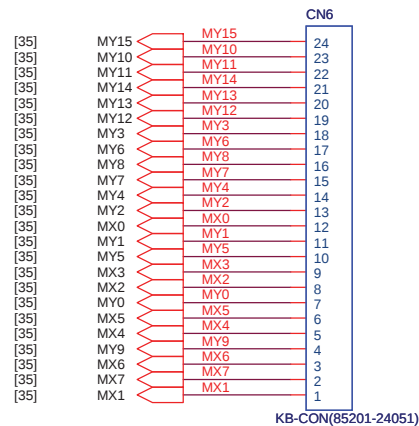


PROJECT :LL3A
Quanta Computer Inc.

Size Custom	Document Number BT/FP/TPM	Rev 1E
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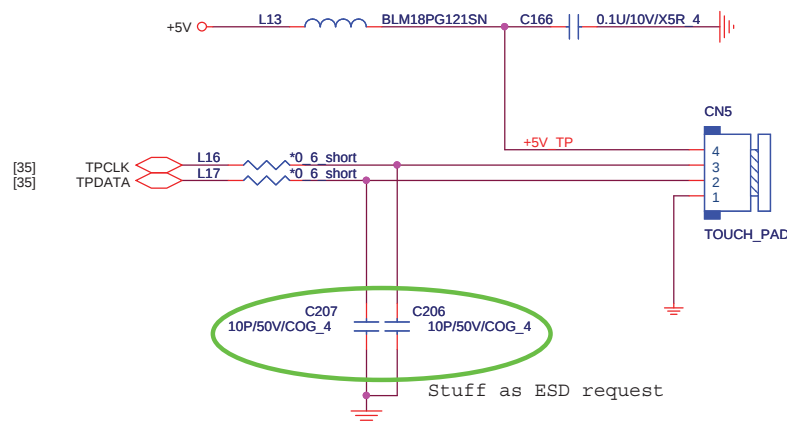


KEYBOARD



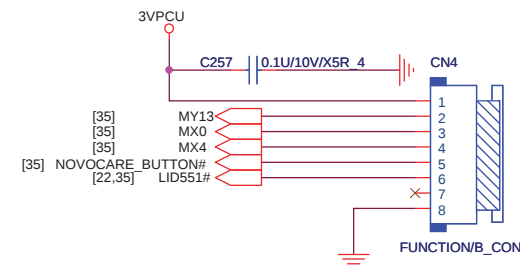
For EMI request

TOUCH PAD

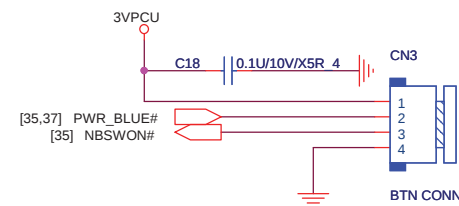


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FUNCTION BUTTON BOARD



POWER BUTTON BOARD



PROJECT :LL3A
Quanta Computer Inc.

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EC-B-09



PROJECT :LL3A
Quanta Computer Inc.

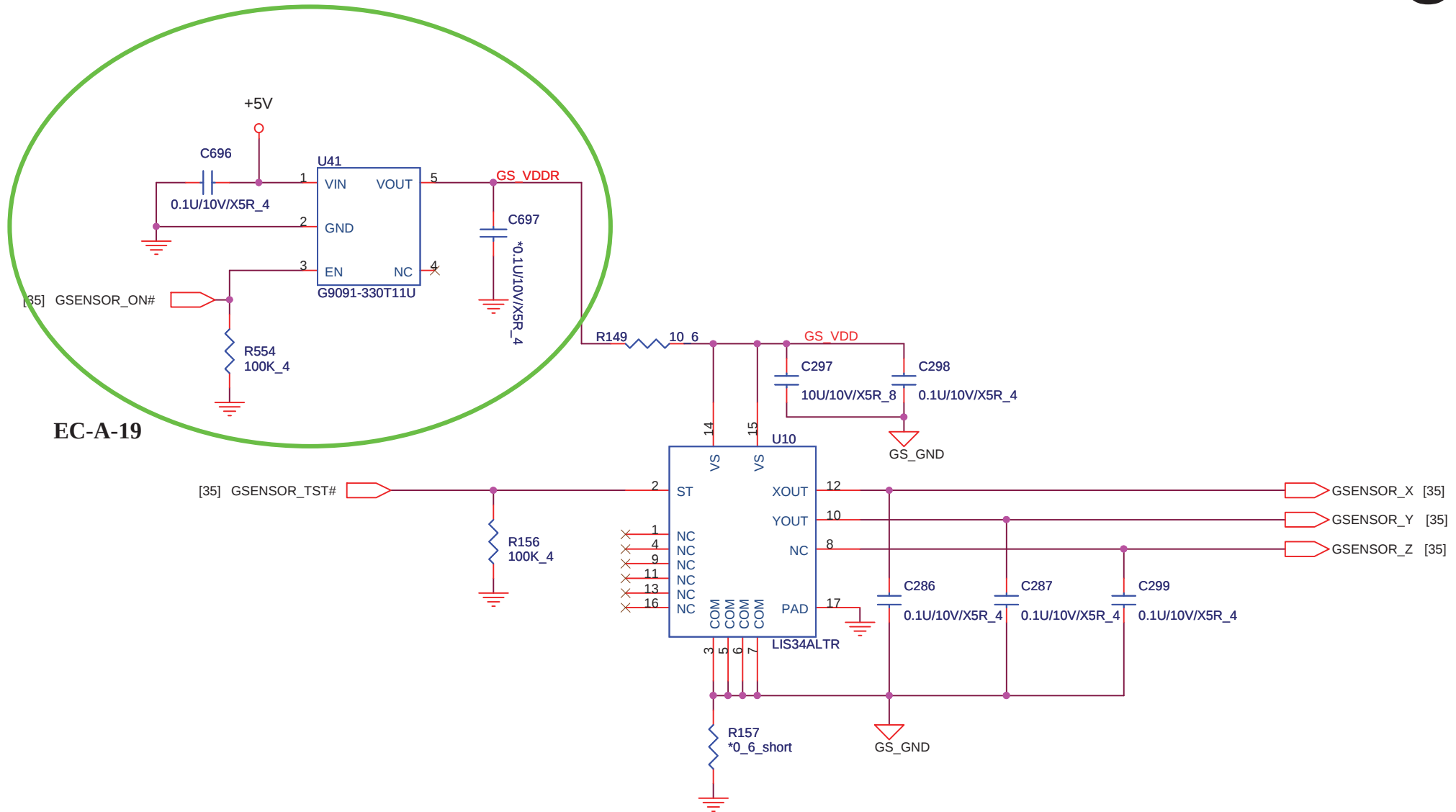
Size Custom	Document Number LED/FAN	Rev 1E
Date:	Tuesday, January 26, 2010	Sheet 37 of 47

G-SENSOR (3-Axial)

<http://hobi-elektronika.net>

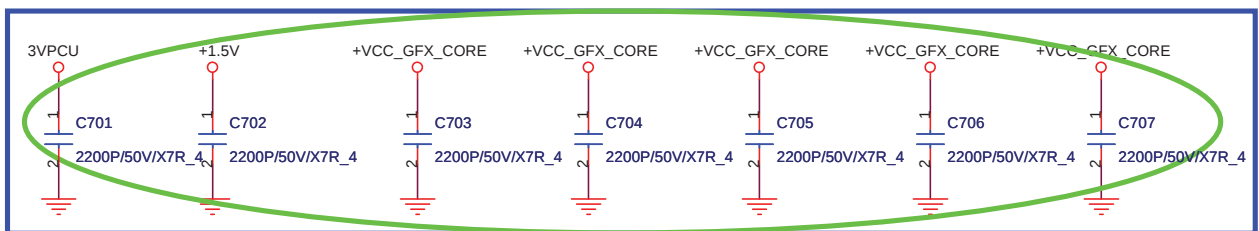
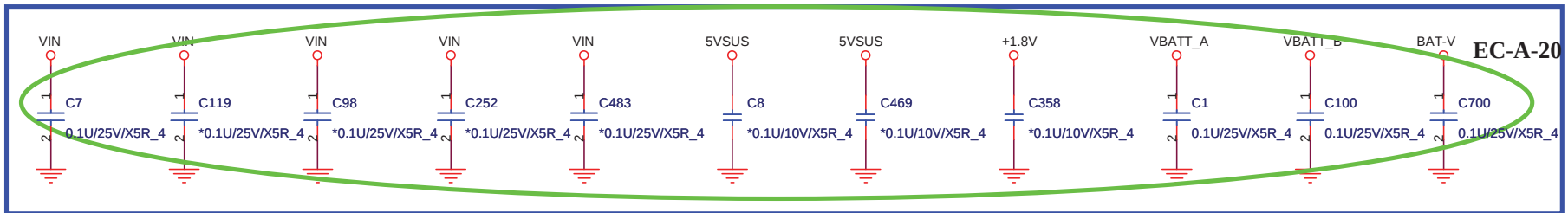
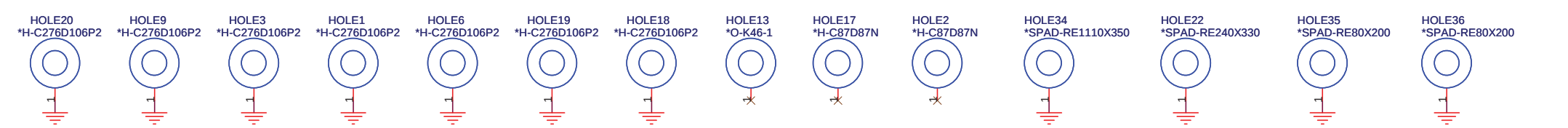
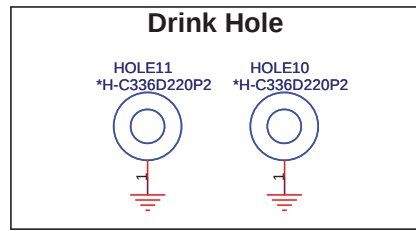
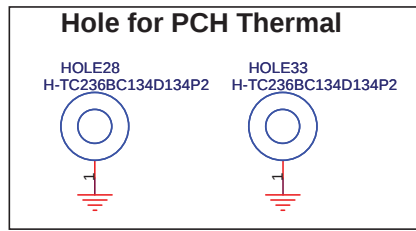
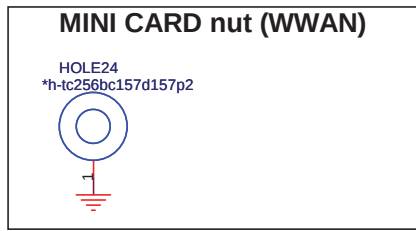
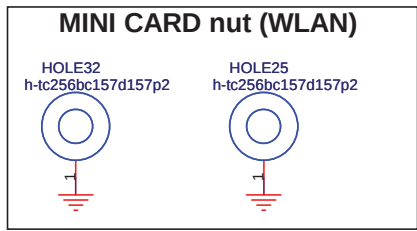
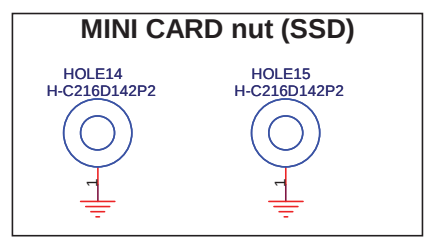
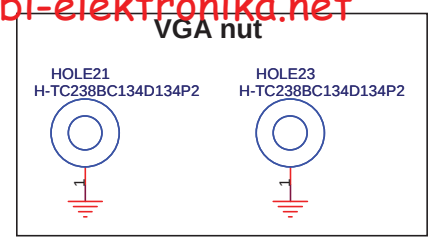
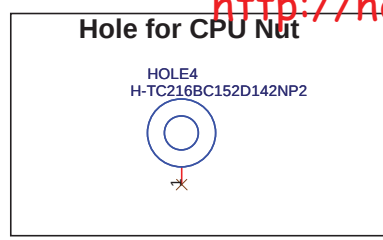
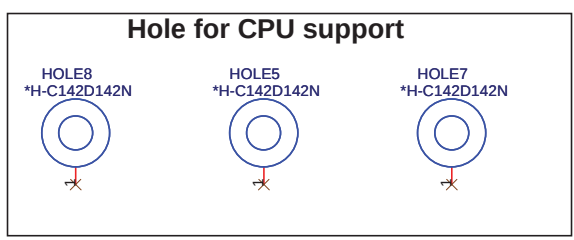
+3V
[22,28,40,43,45]
+15V

38

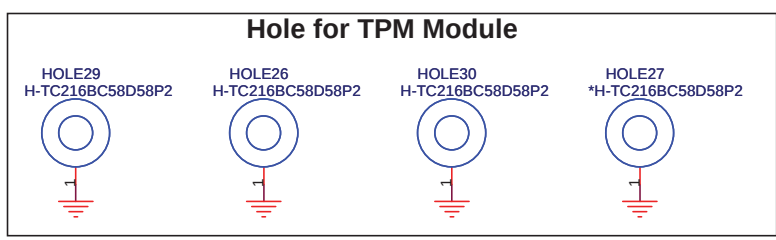


PROJECT :LL3A
Quanta Computer Inc.

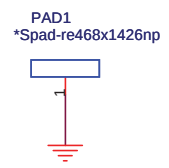
Size Custom	Document Number G-SENSOR	Rev 1E
Date: Tuesday, January 26, 2010	Sheet 38	of 47



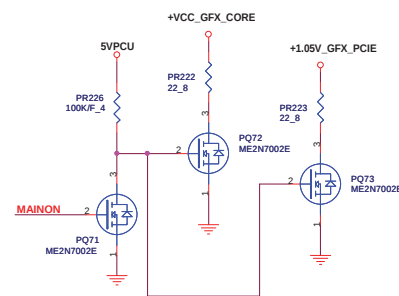
EMI request



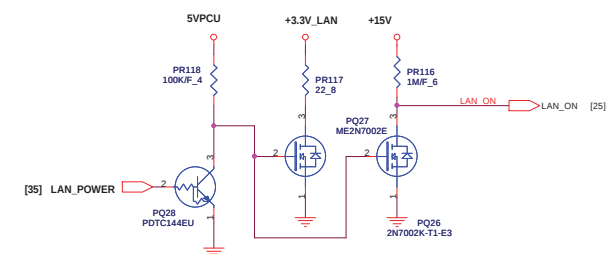
EC-B-10



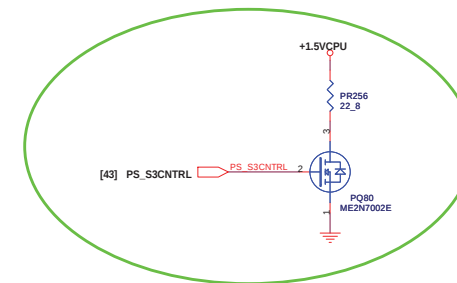
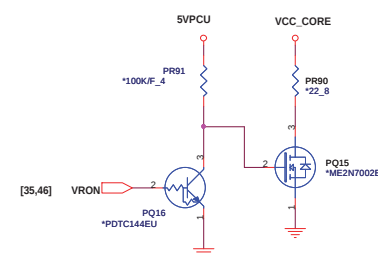
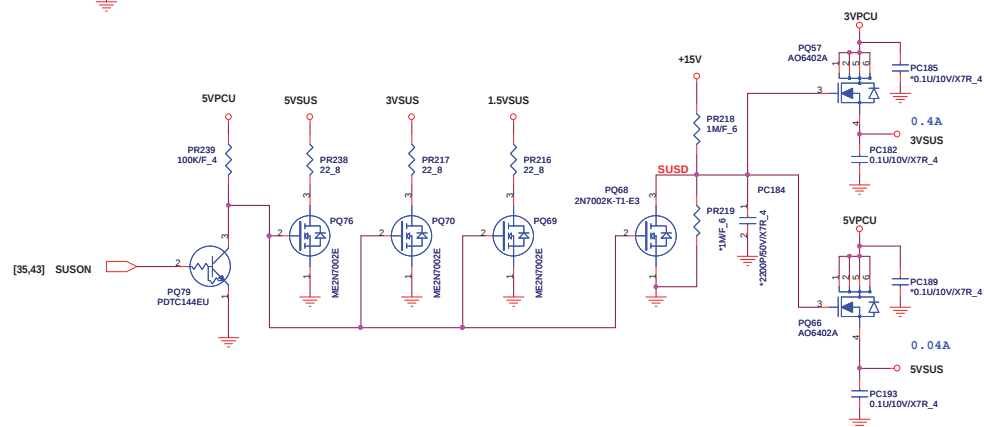
3V_S5, 5V_S5

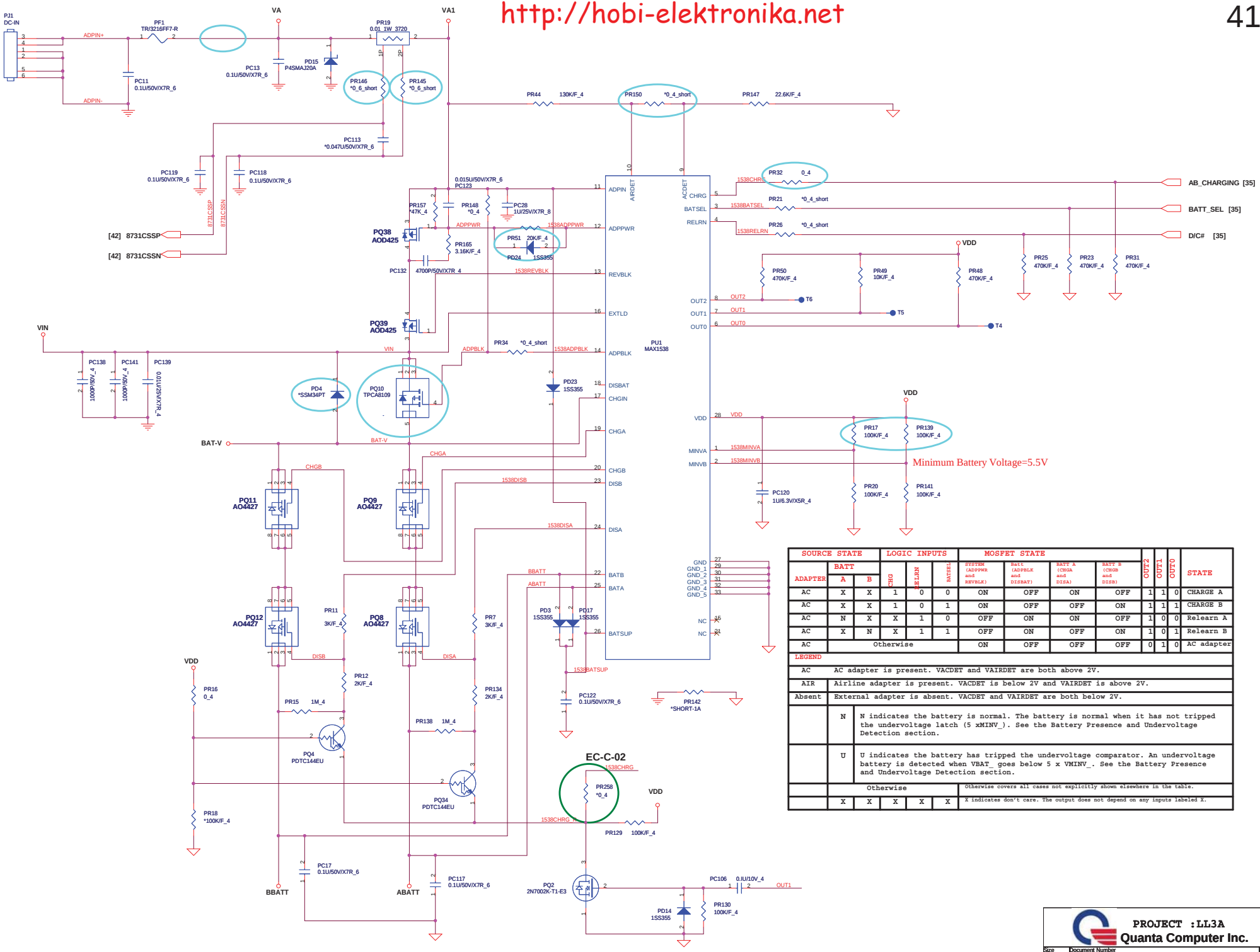


LANVCC

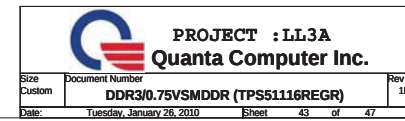


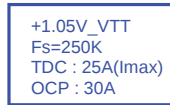
3VSUS, 5VSUS





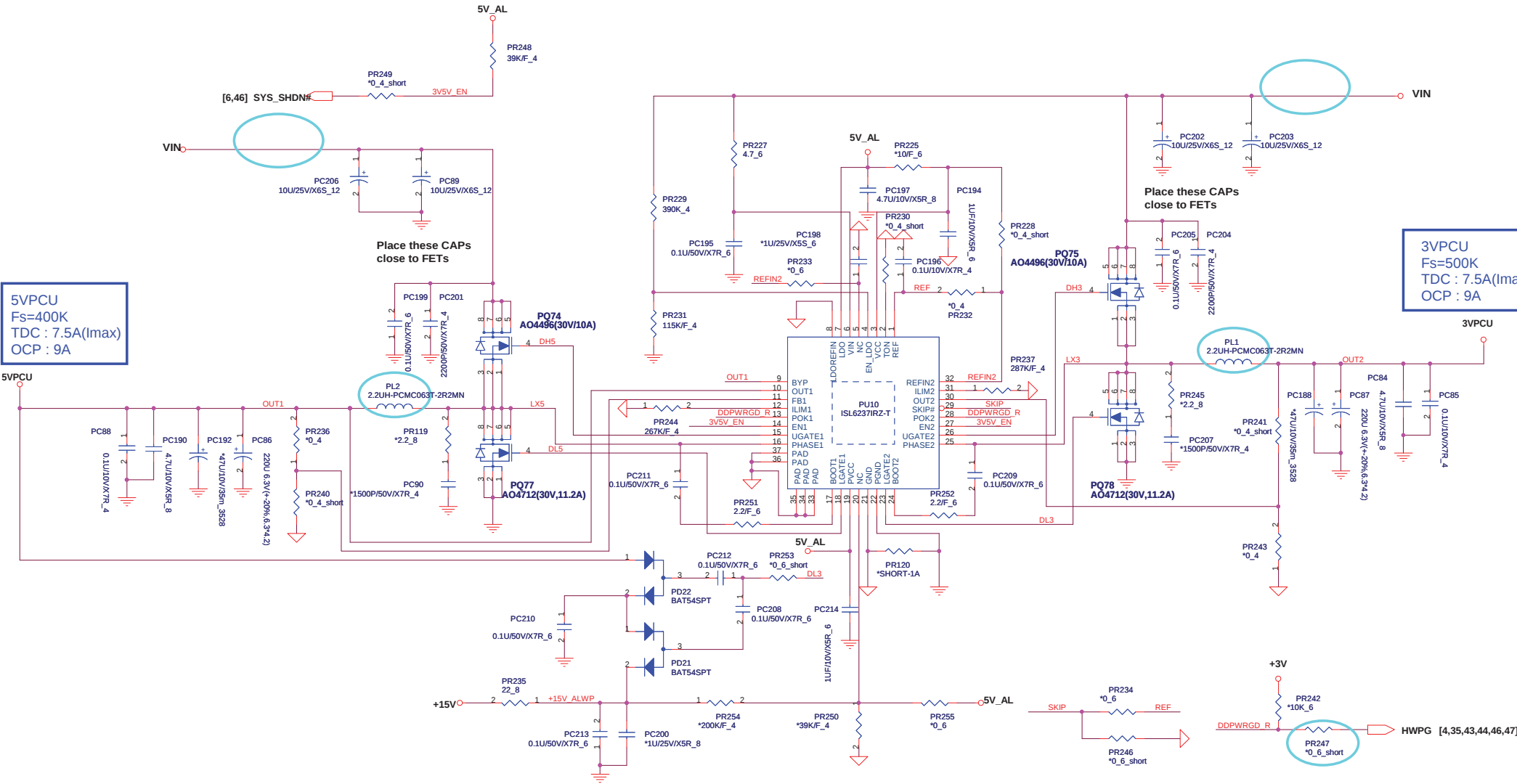
SOURCE STATE			LOGIC INPUTS				MOSFET STATE							STATE		
ADAPTER	BATT		CHG	SEN	BATSEL	SYSTEM (ADPPWR AND REVBLK)	BATT (ADPBLK AND DISBAT)	BATT A (CHGA AND DISA)	BATT B (CHGB AND DISB)	OUT2	OUT1	OUT0				
	A	B														
AC	X	X	1	0	0	ON	OFF	ON	OFF	1	1	0	CHARGE A			
AC	X	X	1	0	1	ON	OFF	OFF	ON	1	1	1	CHARGE B			
AIR	N	X	X	1	0	OFF	ON	ON	OFF	1	0	0	Relearn A			
AC	X	N	X	1	1	OFF	ON	OFF	ON	1	0	1	Relearn B			
AC	Otherwise					ON	OFF	OFF	OFF	0	1	0	AC adapter			
LEGEND																
AC	AC adapter is present. VACDET and VAIRDET are both above 2V.															
AIR	Airline adapter is present. VACDET is below 2V and VAIRDET is above 2V.															
Absent	External adapter is absent. VACDET and VAIRDET are both below 2V.															
	N	N indicates the battery is normal. The battery is normal when it has not tripped the undervoltage latch (5 x mINUV_). See the Battery Presence and Undervoltage Detection section.														
	U	U indicates the battery has tripped the undervoltage comparator. An undervoltage battery is detected when VBAT goes below 5 x mMINUV_. See the Battery Presence and Undervoltage Detection section.														
	Otherwise															
	Otherwise covers all cases not explicitly shown elsewhere in the table.															
X	X	X	X	X	X	X indicates don't care. The output does not depend on any inputs labeled X.										





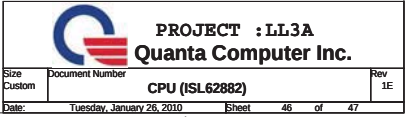
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[9,28,40,43,44,47]
[22,28,40,43]
[22,39,41,42,43,44,46,47]

3VPCU
5VPCU
+15V
VIN



5VPCU
Fs=400K
TDC : 7.5A(I_{max})
OCP : 9A

3VPCU
Fs=500K
TDC : 7.5A(I_{max})
OCP : 9A



For Descrete N10M VGA Only

